

## Model Name: GA-Z87X-UD4H

Rev 1.1 www.xinxunwei.com 400-800-9990

SHEET

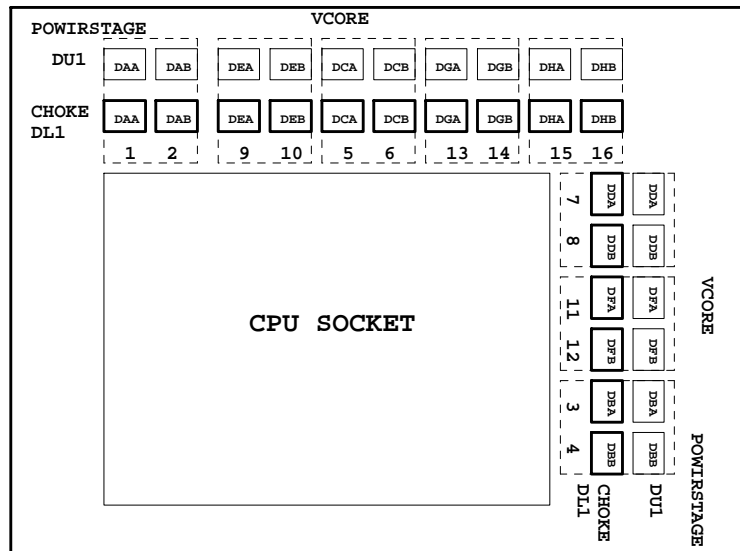
TITLE

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03	BLOCK DIAGRAM
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05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	DP / DVI / HDMI
15	PCI EXPRESS*16/*8 SWITCH
16	PCI EXPRESS*16 SLOT
17	PCI EXPRESS*8 SLOT
18	PCI EXPRESS*4 SLOT
19	PCI EXPRESS*1 SLOTS X3
20	ITE 8892
21	PCI SLOT 1
22	SATA3 SWITCH
23	USB3_eSATA, KB_USB3
24	Marvell 9172
25	ALC898
26	REAR AUDIO JACK
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28	IR3563B PWM Controller
29	IR3553 Power Stage PH_01-04
30	IR3553 Power Stage PH_05-08
31	IR3553 Power Stage PH_09-12
32	IR3553 Power Stage PH_13-16
33	IR3570_DDR PWM Controller
34	IR3598-DDR Driver

SHEET

TITLE

35	DISCRETE POWER I
36	DISCRETE POWER II
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44	FAN CTRL, H/W Monitor
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46	Port80, O.V., Voltage Measure
47	ATX POWER, CLOCK GEN
48	TABLE LIST
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51	VCC1_05_ME, VCC3_ME



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**GA-Z87X-UD4H**

## Component value change history

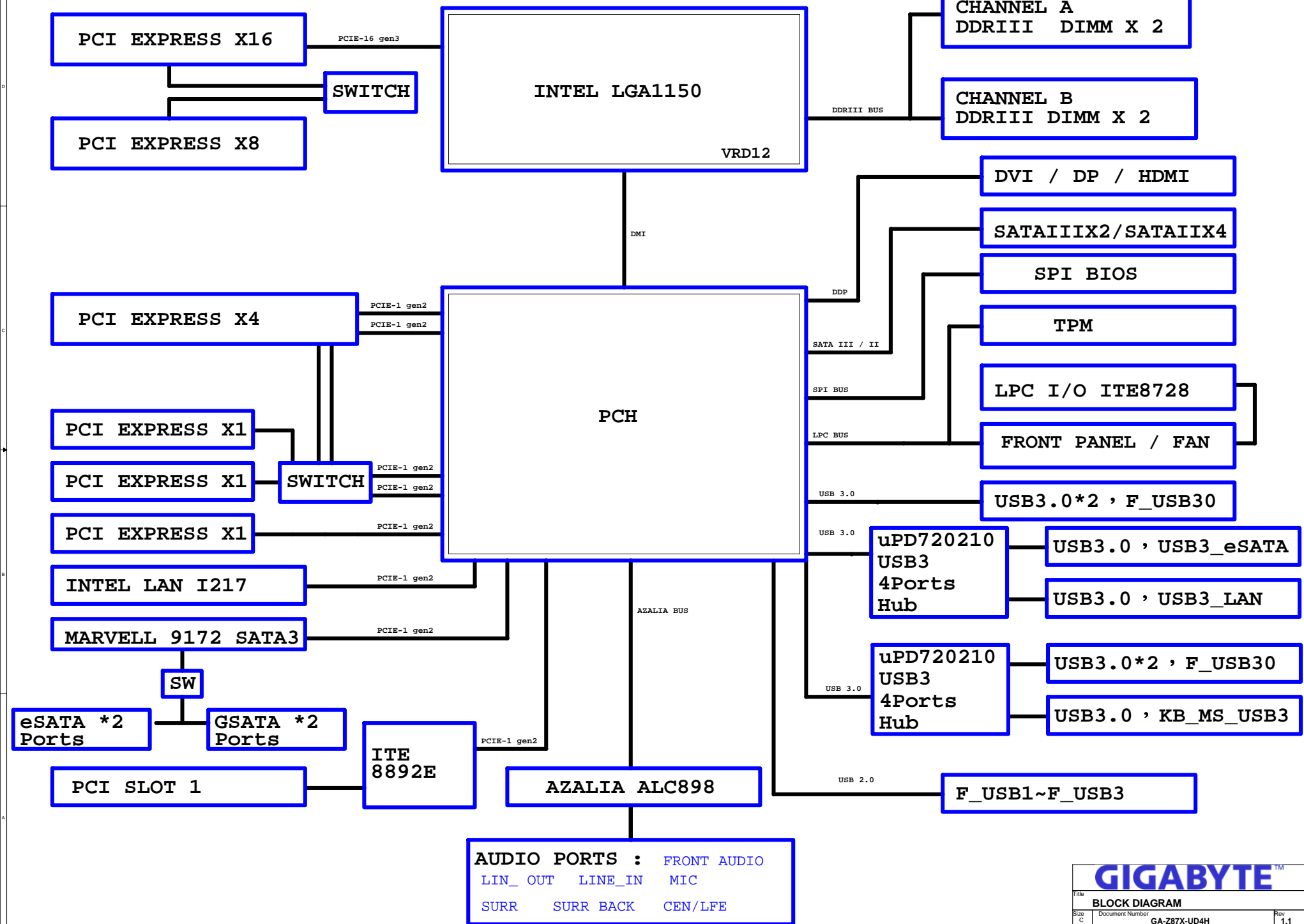
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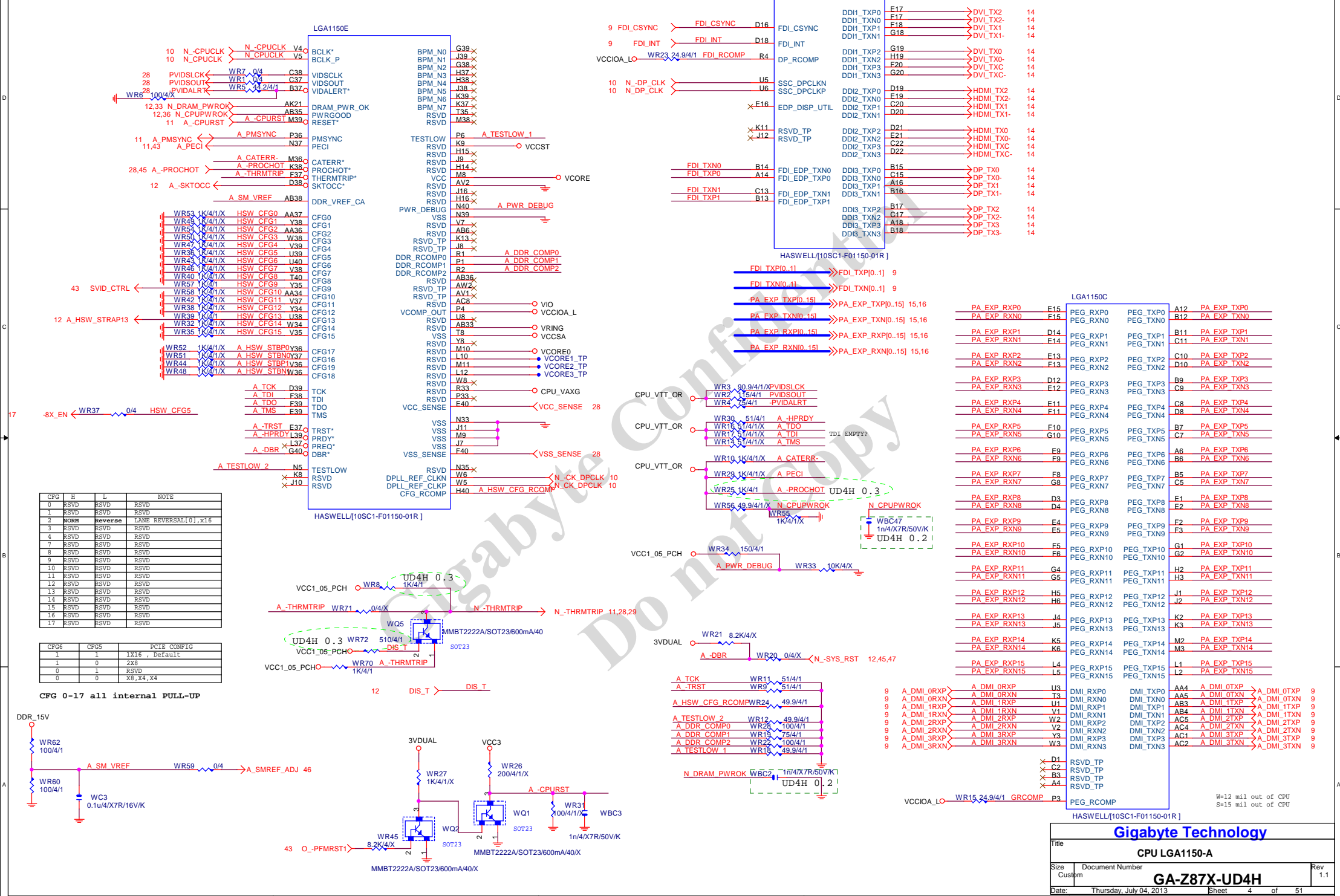
## Circuit or PCB layout change

DATE	Change Item	Reason
2012/11/23	GA-Z87X-UD5H 0.1 gerber out	
12/14	MAC3 MAC4 需改0603	
2013/01/11	KB_MS_USB3之USB signal改接PCH, 並由SIO接Mouse Data & Clock	使PS2下方之USB Port接滑鼠在灌OS時可以動作
	USB3 eSATA & USB3 LAN之SUB Signal則改接uPD720201	使PS2下方之USB Port接滑鼠在灌OS時可以動作
	TI TPS2546 USB Charger 線路修正	
	PCIEx4 Clock改接CLOCKOUT_PCIE_7	For CPU O.C.時可以Fix 100MHz
	TP1 change to Top side	For工廠可以量測之用
	PCIex4之Present#改接GPIO48	PCIex4之Present#改接GPIO48
	PCH_SINK & MOS_SINK尺寸加大	增加賣相
	3VDUAL_PCH電源移到PCH_SINK以外	因100uF電容超過PCH_SINK之4mm限高
2013/01/14	8 Series Note 33	
2013/01/15	Dual BIOS change Dual UEFI BIOS	
2013/02/1	IR DD_DU1 DF_DU1 DB_DU1 PIN1標示	
2013/02/7	RB 測點背板TP1	
2013/02/8	熱敏電阻移位	
2013/02/8	ICT測點加到90%	
20130305_0.3	HDMI & DVI 走線盡量截彎取直	
	增加N_PCH_PWROK控制線路, 防止DDR15V漏電線路	
	增加Audio Amplifier之anti-pop noise線路	
	增加+12V之Dummy Load控制線路	
	TI TPS2545 USB charger線路修正	
	F_PANEL update new library, Power Led線路修正	
	IT8728有些GPIO有修改換pin	
	所有FAN Connector之電源pin增加0 ohm做為防燒Trace之保護	
	DDR3 Layout比照Z87X-OC 9.3版	
20130312_1.0	Add 2 uPD720210 USB3.0 Hub, add F_USB30_2	Fix Intel PCH S3 resume, USB device re-plug issue
	Add VCC1_05_ME, VCC3_ME	Support Intel Remote Wake-up function.
	PCIEX1_1 change to x2, Revmov 1 Q-SW	
	Dual UEFI BIOS change UEFI DualBIOS	
20130329_1.01	OR52 change to Non-ShortPAD (33 ohm)	解決PSU(C37) VCC3電壓高至4V問題
	Add N_USBOC_F pull-up R.	
20130401_1.01	Add Fan 電容	

# BLOCK DIAGRAM

www.xinxunwei.com 400-800-9990





LGA1150A

MAAA0	AU13	DDR0_M0	DDR0_D00	AD38	MDA0
MAAA1	AV16	DDR0_M1	DDR0_D01	AD39	MDA1
MAAA2	AU16	DDR0_M2	DDR0_D02	AF38	MDA2
MAAA3	AW17	DDR0_M3	DDR0_D03	AF39	MDA3
MAAA4	AU17	DDR0_M4	DDR0_D04	AD37	MDA4
MAAA5	AW18	DDR0_M5	DDR0_D05	AD40	MDA5
MAAA6	AV17	DDR0_M6	DDR0_D06	AF37	MDA6
MAAA7	AT18	DDR0_M7	DDR0_D07	AF40	MDA7
MAAA8	AU18	DDR0_M8	DDR0_D08	AH40	MDA9
MAAA9	AT19	DDR0_M9	DDR0_D09	AH39	MDA13
MAAA10	AW11	DDR0_M10	DDR0_D10	AK38	MDA10
MAAA11	AV19	DDR0_M11	DDR0_D11	AK39	MDA11
MAAA12	AU19	DDR0_M12	DDR0_D12	AH37	MDA12
MAAA13	AY10	DDR0_M13	DDR0_D13	AH38	MDA8
MAAA14	AT20	DDR0_M14	DDR0_D14	AK37	MDA14
MAAA15	AU21	DDR0_M15	DDR0_D15	AK40	MDA15
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17
MODT_A1	AY8	DDR0_ODT1	DDR0_D17	AM39	MDA21
MODT_A2	AW9	DDR0_ODT2	DDR0_D18	AP38	MDA18
MODT_A3	AU8	DDR0_ODT3	DDR0_D19	AP39	MDA19
			DDR0_D20	AM37	MDA20
			DDR0_D21	AM38	MDA16
			DDR0_D22	AP37	MDA22
			DDR0_D23	AP40	MDA23
			DDR0_D24	AV37	MDA25
			DDR0_D25	AV37	MDA29
			DDR0_D26	AU35	MDA26
			DDR0_D27	AV35	MDA27
			DDR0_D28	AT37	MDA28
			DDR0_D29	AU37	MDA24
			DDR0_D30	AT35	MDA30
			DDR0_D31	AW35	MDA31
			DDR0_D32	AY6	MDA33
			DDR0_D33	AU6	MDA37
			DDR0_D34	AY4	MDA34
			DDR0_D35	AU4	MDA35
			DDR0_D36	AW6	MDA36
			DDR0_D37	AV6	MDA32
			DDR0_D38	AW4	MDA38
			DDR0_D39	AY4	MDA39
			DDR0_D40	AR1	MDA41
			DDR0_D41	AR4	MDA45
			DDR0_D42	AN3	MDA42
			DDR0_D43	AN4	MDA43
			DDR0_D44	AR2	MDA44
			DDR0_D45	AR3	MDA40
			DDR0_D46	AN2	MDA46
			DDR0_D47	AN1	MDA47
			DDR0_D48	AL1	MDA49
			DDR0_D49	AL4	MDA53
			DDR0_D50	AJ3	MDA50
			DDR0_D51	AJ4	MDA51
			DDR0_D52	AL3	MDA48
			DDR0_D53	AJ2	MDA54
			DDR0_D54	AJ1	MDA55
			DDR0_D55	AG1	MDA57
			DDR0_D56	AG4	MDA61
			DDR0_D57	AE3	MDA58
			DDR0_D58	AE4	MDA59
			DDR0_D59	AG2	MDA60
			DDR0_D60	AG3	MDA56
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			DDR0_D62	AE1	MDA63
			DDR0_D63	AE39	DQSA0
			DDR0_D64	AJ39	DQSA1
			DDR0_D65	AN39	DQSA2
			DDR0_D66	AV36	DQSA3
			DDR0_D67	AV5	DQSA4
			DDR0_D68	AP3	DQSA5
			DDR0_D69	AK3	DQSA6
			DDR0_D70	AF3	DQSA7
			DDR0_D71	AV32	DQSA0
			DDR0_D72	AE38	DQSA1
			DDR0_D73	AJ38	DQSA2
			DDR0_D74	AN38	DQSA3
			DDR0_D75	AU36	DQSA4
			DDR0_D76	AW5	DQSA5
			DDR0_D77	AP2	DQSA6
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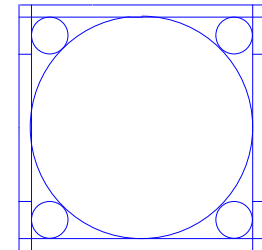
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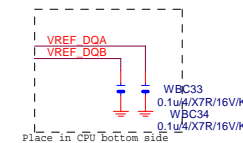
LGA1150B

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MAAB1	AK23	DDR1_M1	DDR1_D01	AE35	MDB1
MAAB2	AM22	DDR1_M2	DDR1_D02	AG35	MDB2
MAAB3	AM23	DDR1_M3	DDR1_D03	AH35	MDB3
MAAB4	AP23	DDR1_M4	DDR1_D04	AD34	MDB4
MAAB5	AL23	DDR1_M5	DDR1_D05	AD35	MDB5
MAAB6	AY24	DDR1_M6	DDR1_D06	AG34	MDB6
MAAB7	AV25	DDR1_M7	DDR1_D07	AH34	MDB7
MAAB8	AU26	DDR1_M8	DDR1_D08	AL34	MDB8
MAAB9	AW25	DDR1_M9	DDR1_D09	AK35	MDB9
MAAB10	AP18	DDR1_M10	DDR1_D10	AL31	MDB10
MAAB11	AY25	DDR1_M11	DDR1_D11	AL31	MDB11
MAAB12	AV26	DDR1_M12	DDR1_D12	AK34	MDB12
MAAB13	AR15	DDR1_M13	DDR1_D13	AK35	MDB13
MAAB14	AV27	DDR1_M14	DDR1_D14	AK32	MDB14
MAAB15	AY28	DDR1_M15	DDR1_D15	AL32	MDB15
			DDR1_D16	AN34	MDB17
			DDR1_D17	AP34	MDB21
			DDR1_D18	AN31	MDB19
			DDR1_D19	AP31	MDB23
			DDR1_D20	AN35	MDB20
			DDR1_D21	AP35	MDB18
			DDR1_D22	AN32	MDB19
			DDR1_D23	AP32	MDB22
			DDR1_D24	AM29	MDB25
			DDR1_D25	AM28	MDB28
			DDR1_D26	AR29	MDB27
			DDR1_D27	AR28	MDB30
			DDR1_D28	AL29	MDB24
			DDR1_D29	AL28	MDB29
			DDR1_D30	AP29	MDB26
			DDR1_D31	AP28	MDB31
			DDR1_D32	AR12	MDB32
			DDR1_D33	AP12	MDB33
			DDR1_D34	AL12	MDB34
			DDR1_D35	AL12	MDB35
			DDR1_D36	AR13	MDB36
			DDR1_D37	AP13	MDB37
			DDR1_D38	AM13	MDB38
			DDR1_D39	AM12	MDB39
			DDR1_D40	AR9	MDB45
			DDR1_D41	AP9	MDB41
			DDR1_D42	AR6	MDB47
			DDR1_D43	AP6	MDB43
			DDR1_D44	AR10	MDB44
			DDR1_D45	AP10	MDB40
			DDR1_D46	AR7	MDB46
			DDR1_D47	AP7	MDB42
			DDR1_D48	AM9	MDB52
			DDR1_D49	AL9	MDB53
			DDR1_D50	AL6	MDB50
			DDR1_D51	AL7	MDB55
			DDR1_D52	AM10	MDB48
			DDR1_D53	AL10	MDB49
			DDR1_D54	AM6	MDB54
			DDR1_D55	AM7	MDB51
			DDR1_D56	AH6	MDB61
			DDR1_D57	AH7	MDB59
			DDR1_D58	AE5	MDB63
			DDR1_D59	AE7	MDB63
			DDR1_D60	AJ6	MDB56
			DDR1_D61	AJ7	MDB57
			DDR1_D62	AF6	MDB58
			DDR1_D63	AF7	MDB62
			DDR1_D64	AF35	DQSB0
			DDR1_D65	AL33	DQSB1
			DDR1_D66	AP33	DQSB2
			DDR1_D67	AN28	DQSB3
			DDR1_D68	AN12	DQSB4
			DDR1_D69	AP8	DQSB5
			DDR1_D70	AL8	DQSB6
			DDR1_D71	AG7	DQSB7
			DDR1_D72	AN25	
			DDR1_D73	AF34	DQSB0
			DDR1_D74	AK33	DQSB1
			DDR1_D75	AN33	DQSB2
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			DDR1_D77	AN13	DQSB4
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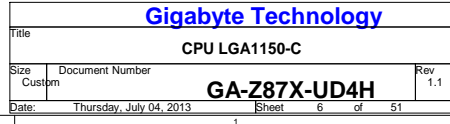
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Need check the new CPU ME

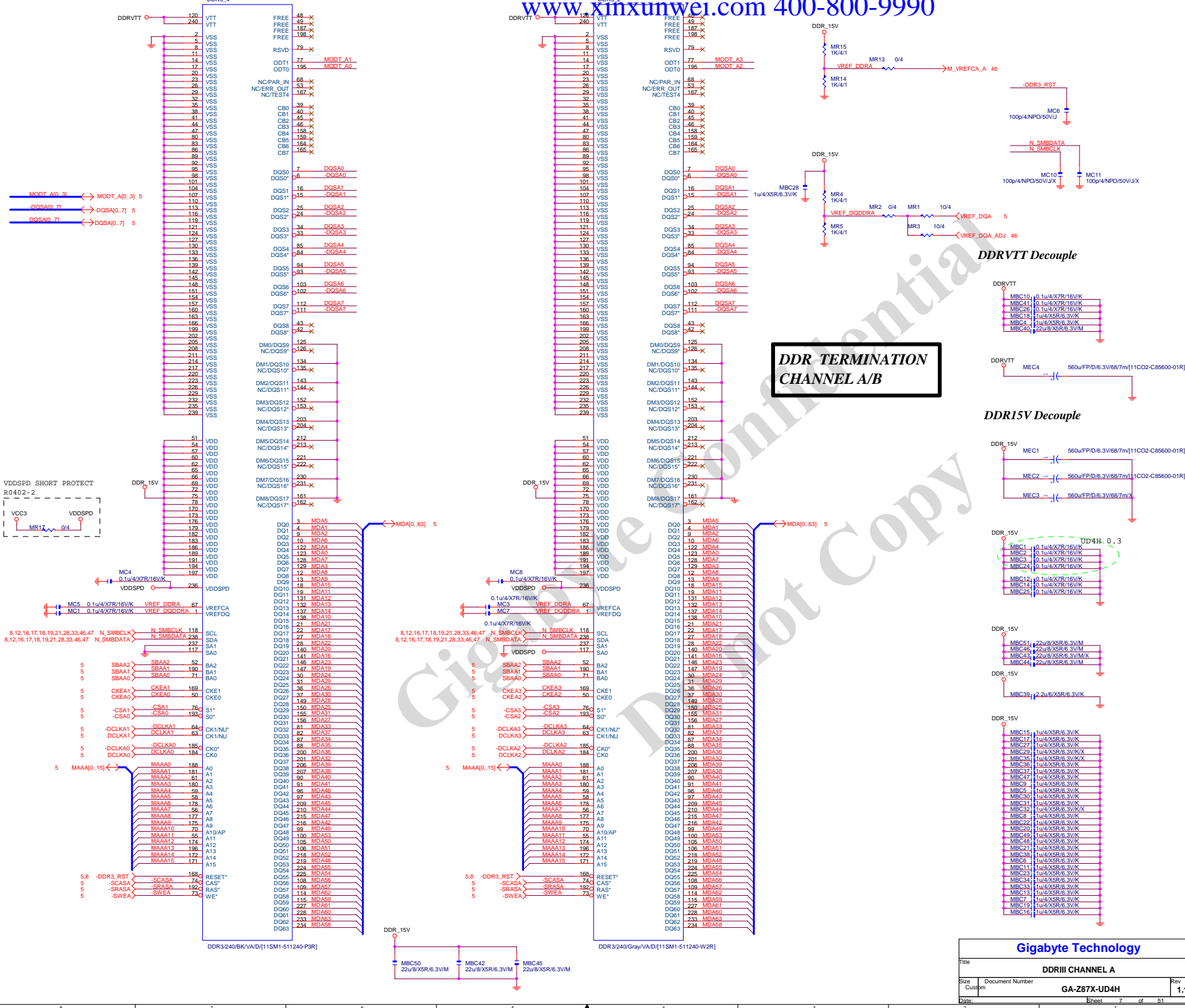


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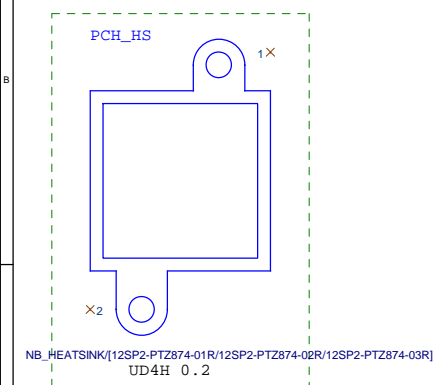
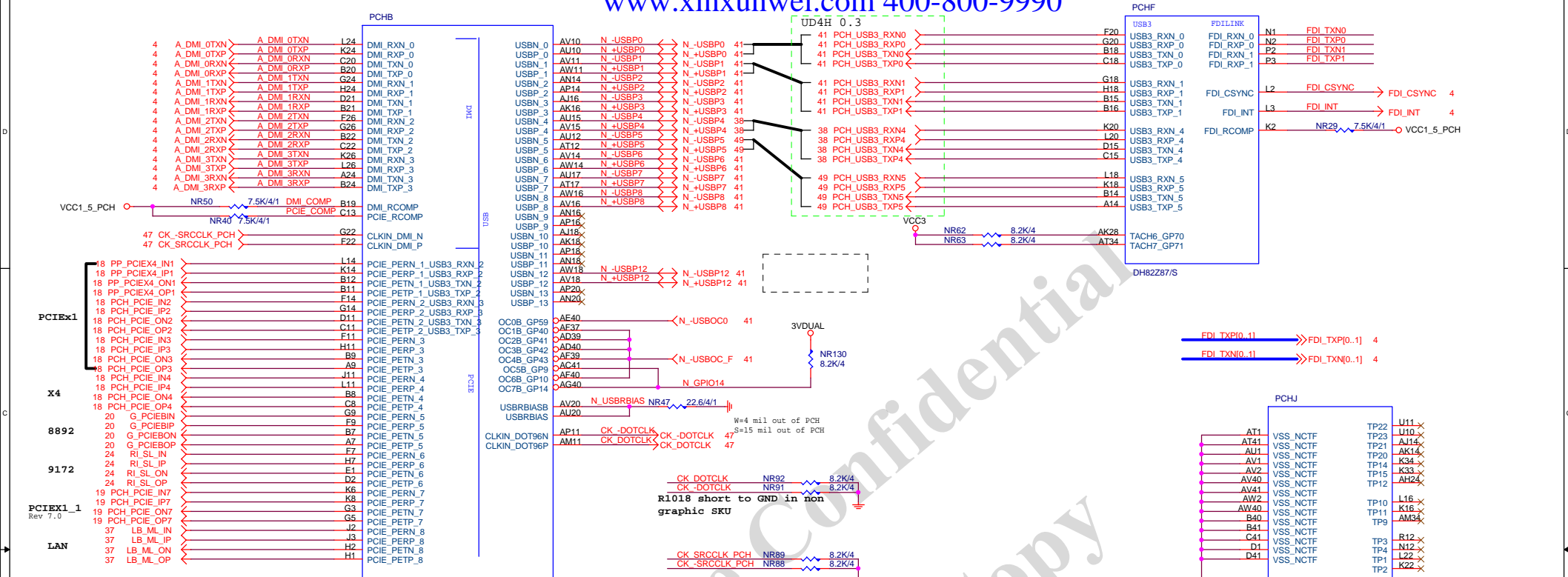




<b>Gigabyte Technology</b>			
Title <b>DDRIII CHANNEL A</b>			
Size Custom	Document Number <b>GA-Z87X-UD4H</b>		Rev <b>1:</b>
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## PCH & Hub USB3.0 Distribution

Port0 : F\_USB30\_2 ( Left )

Port1 : F_USB30_2 (Right)
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Port4 : uPD720210 Hub (HAU1)
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Port4 : USB3 ESATA (R)

Port3 : USB3\_ESATA(L)

Port2 : USB30\_LAN (R)

└─Port1 : USB30\_LAN (L)

Port5 : uPD720210 Hub (HCU1)
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Port4B : KB\_MS\_USB3 (R)

Port3B : KB\_MS\_USB3 (L)

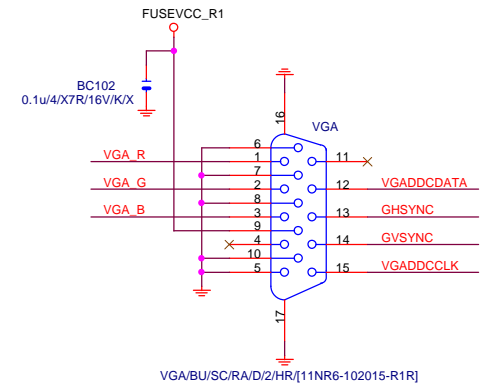
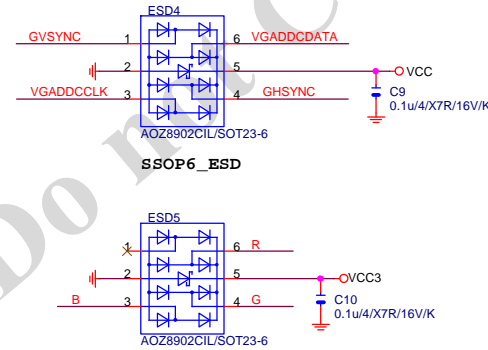
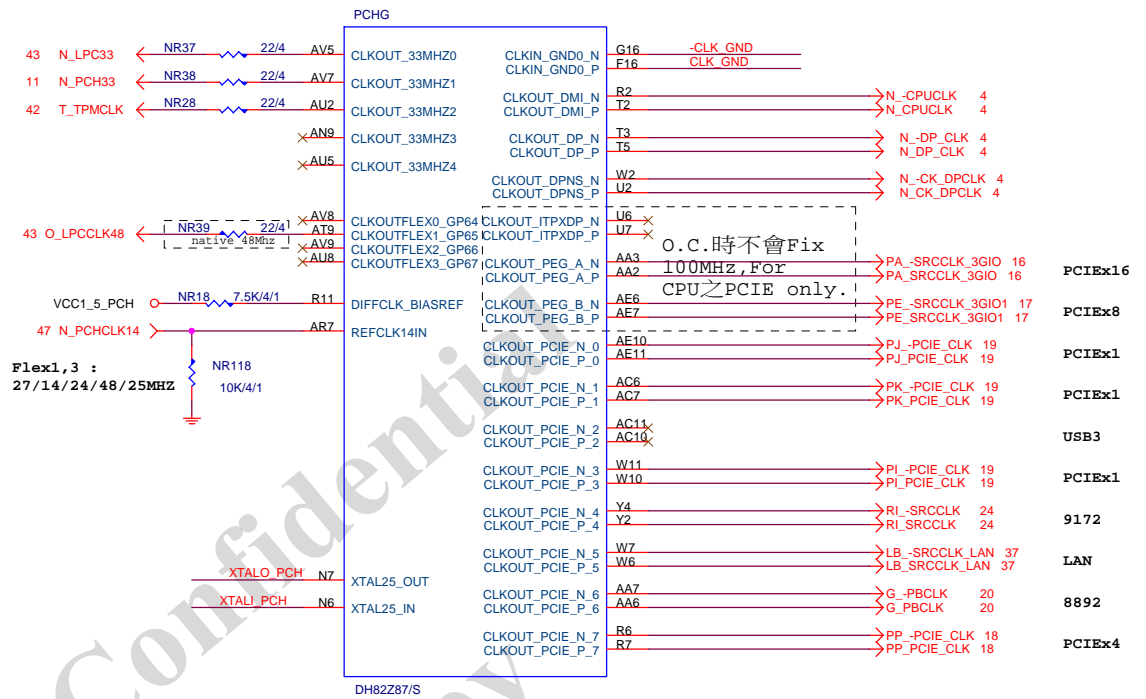
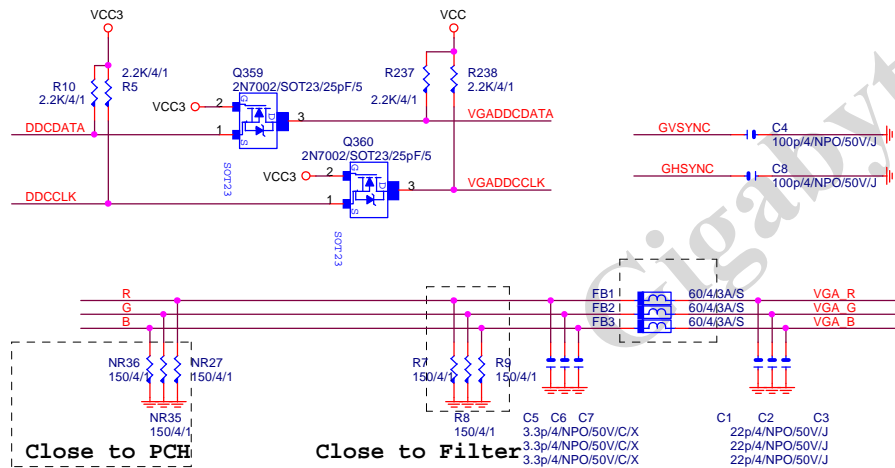
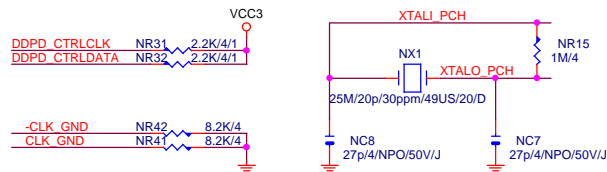
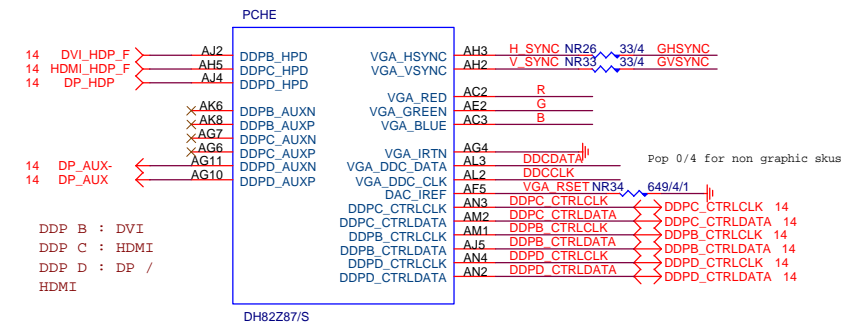
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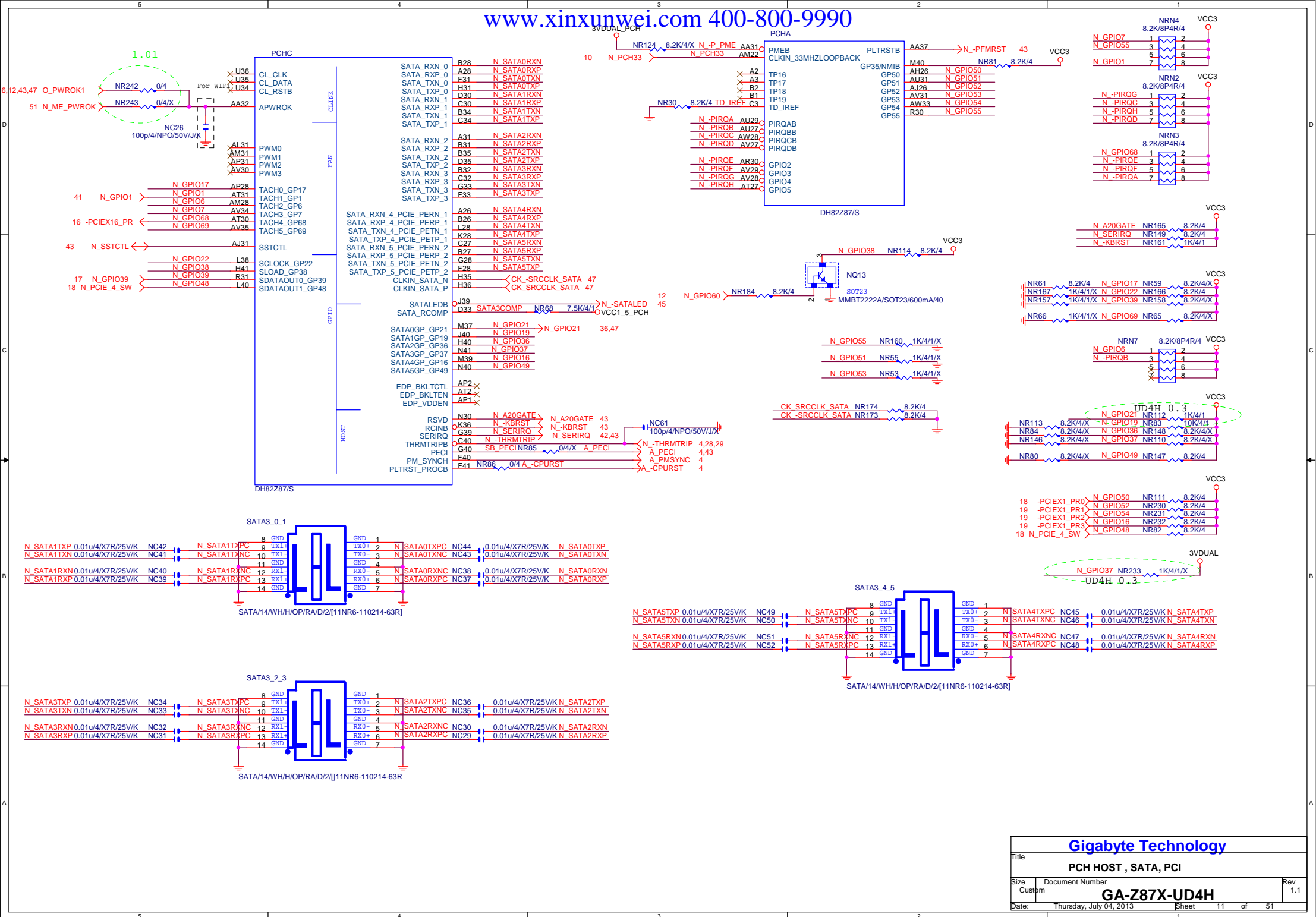
|-----Port2B : F_USB30_1  (R)

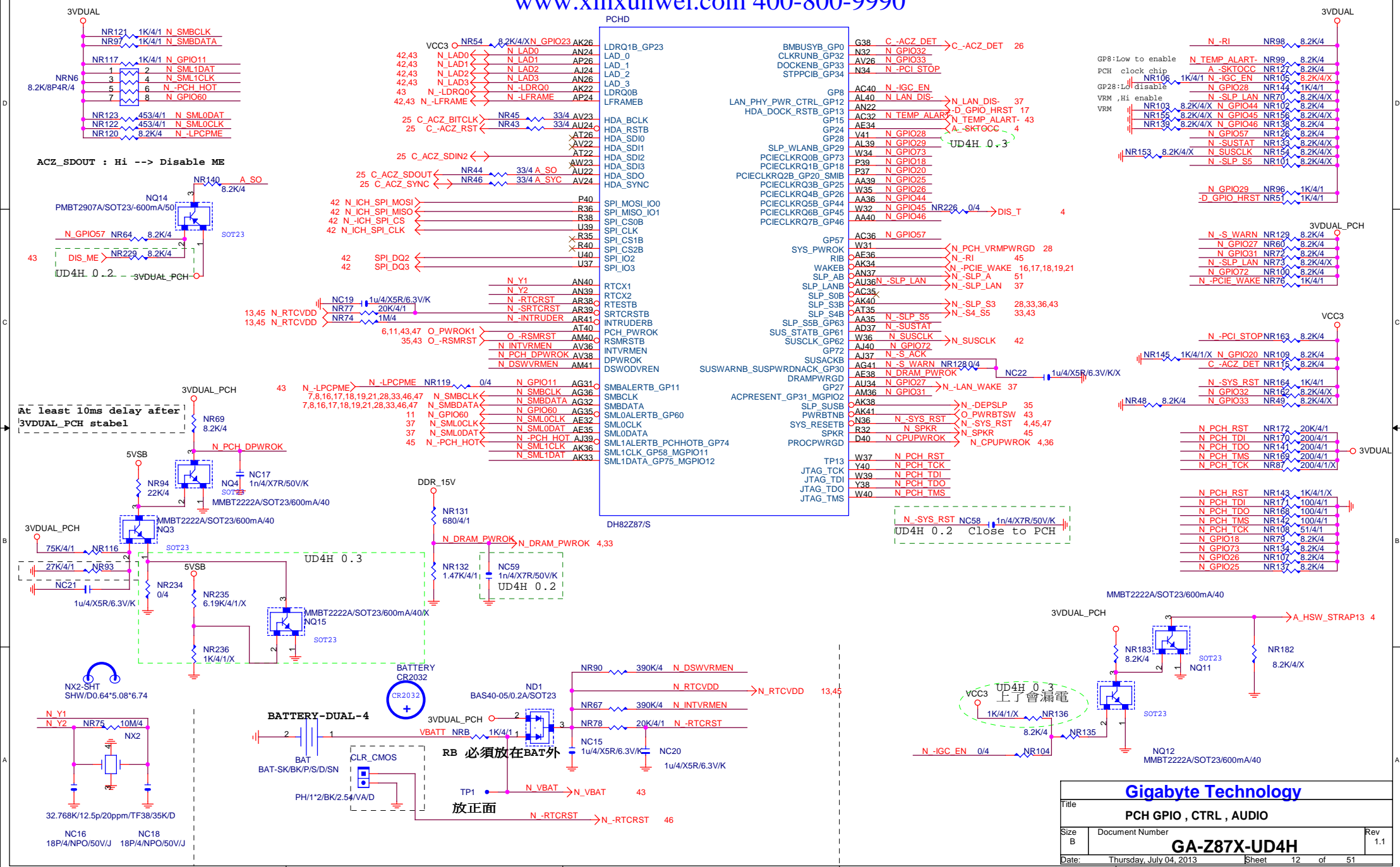
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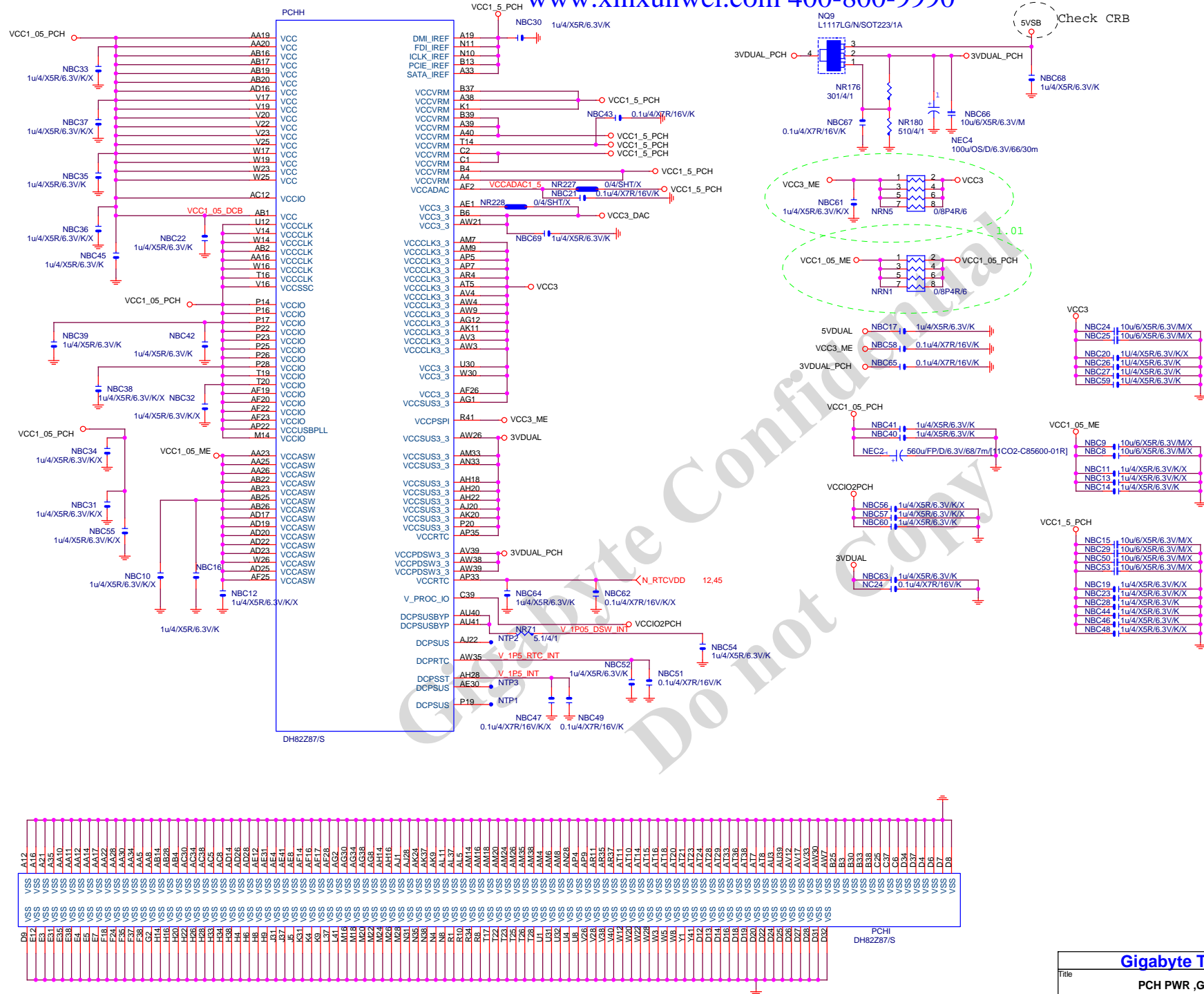
Port1B : F\_USB30\_1 (L)

USB3	00 01	04	05	F_USB1 F_USB2
USB2	00 01	12 13 10 11	02 03 08 09	04 05 06 07
OC	OC0	OC6	OC5	OC_1_4 OC_F

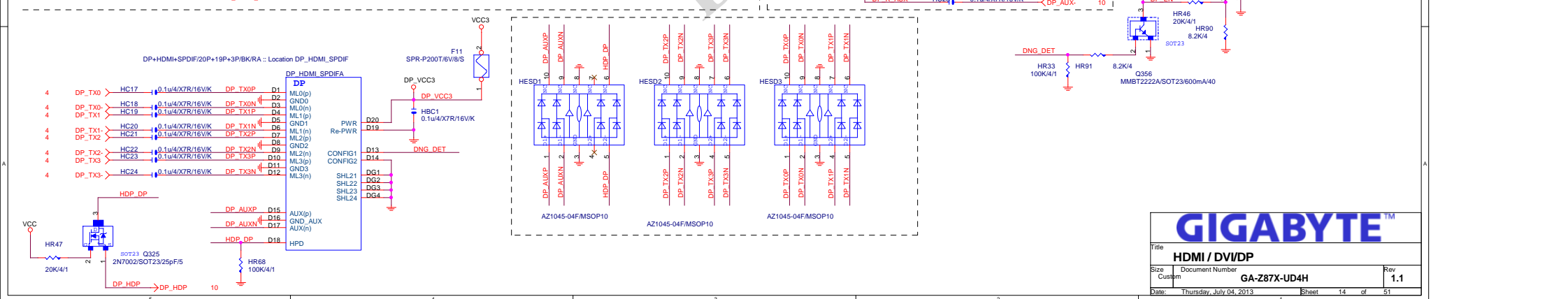
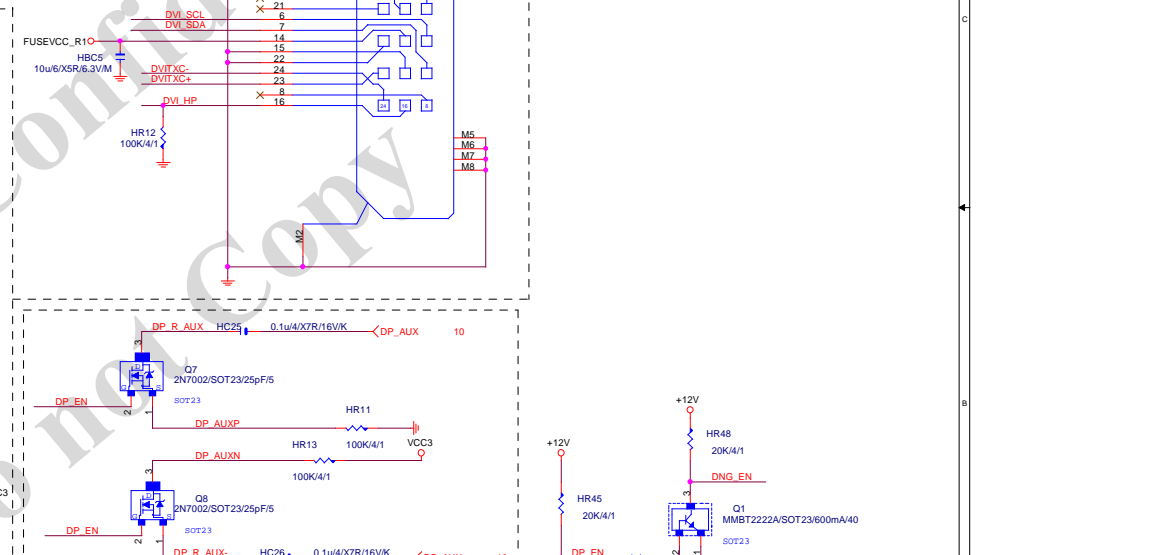
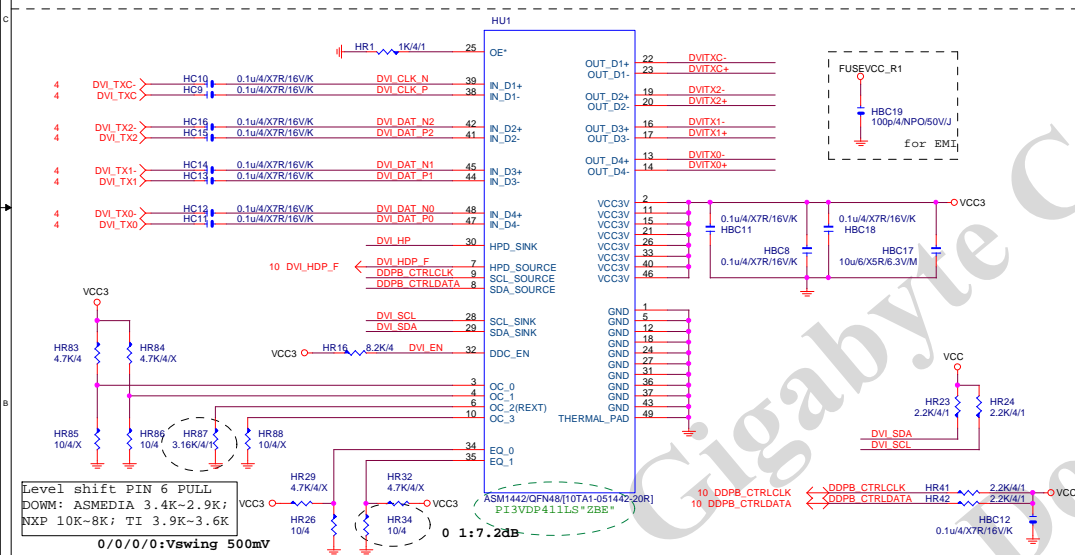
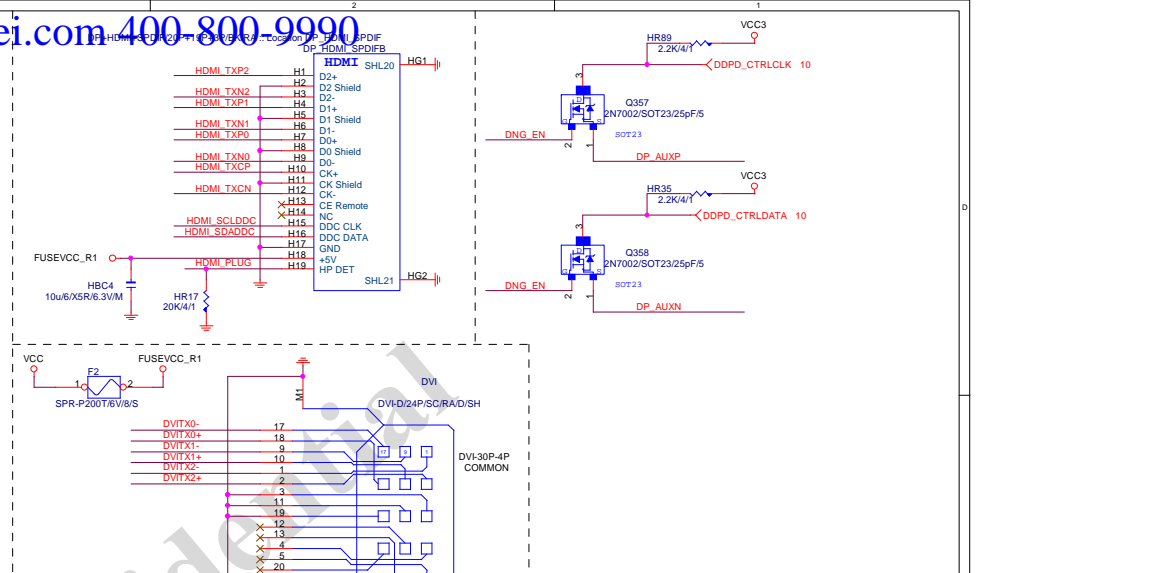
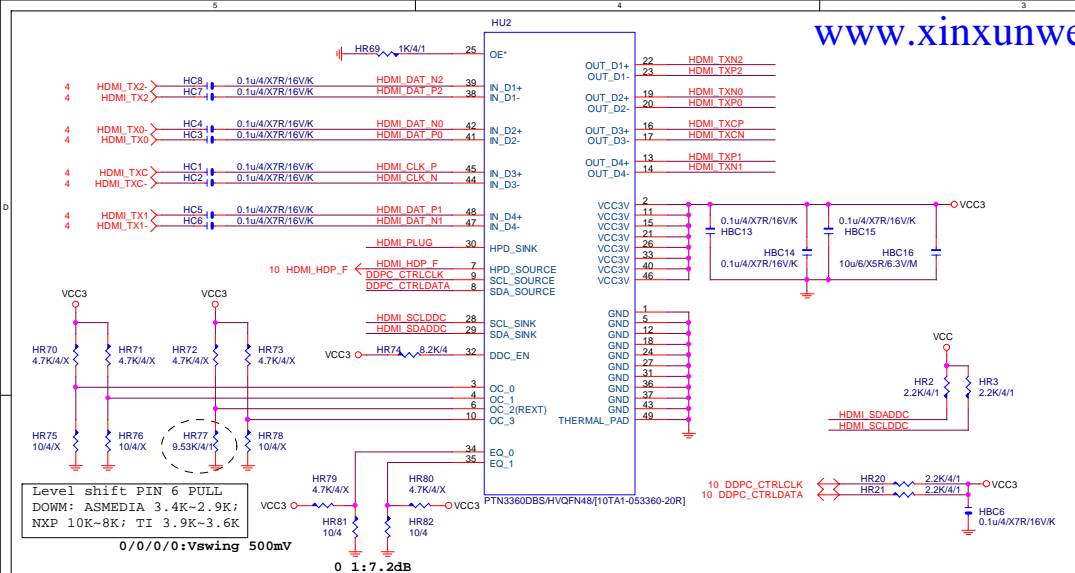


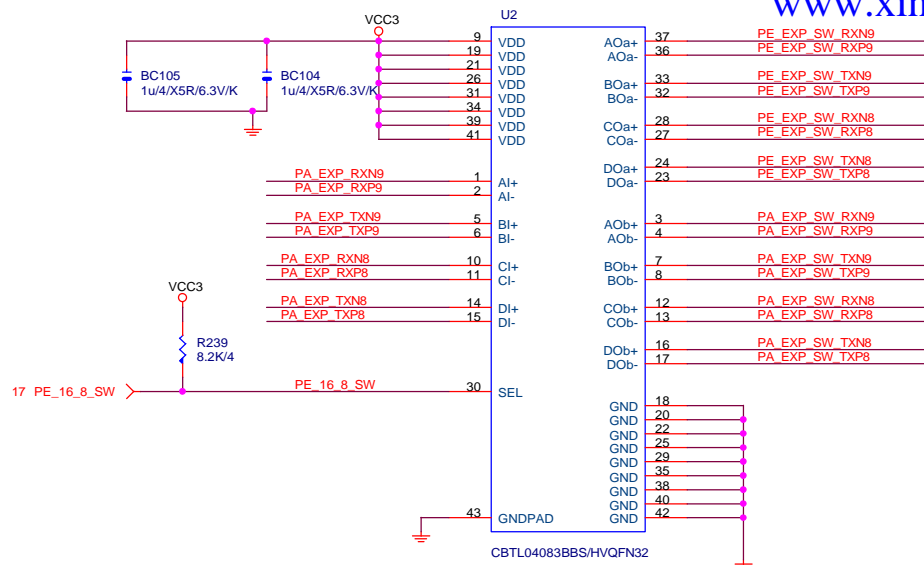




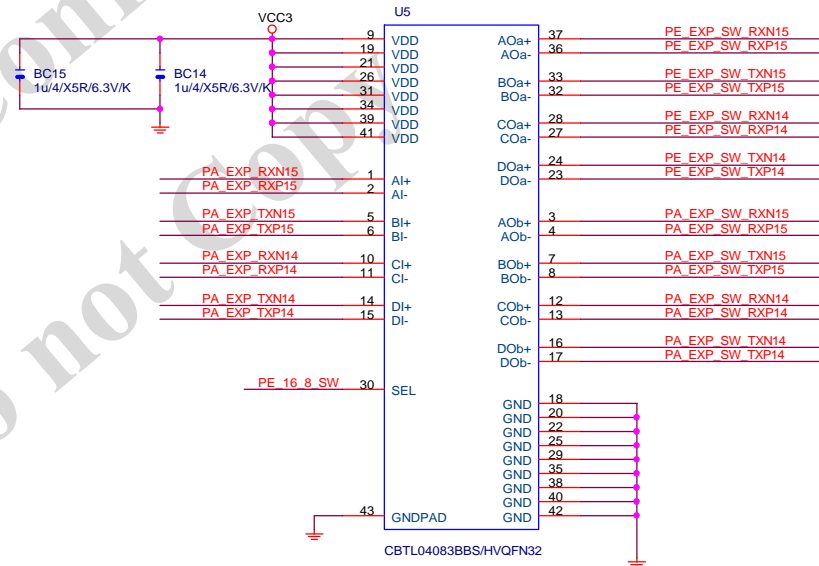
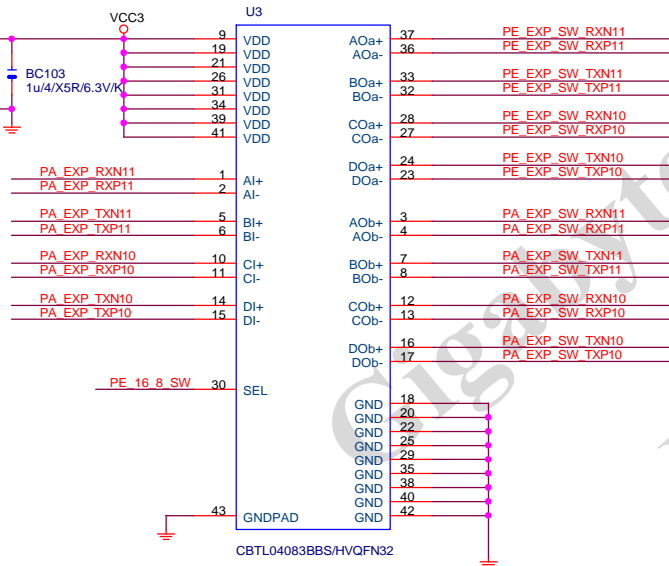
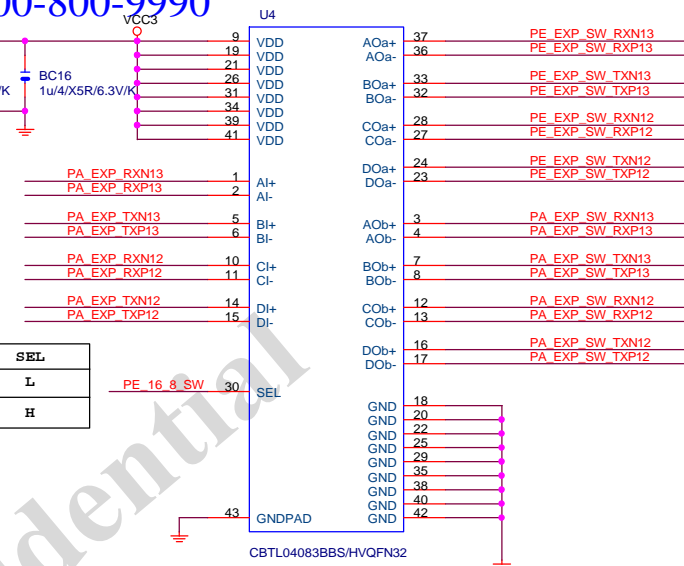








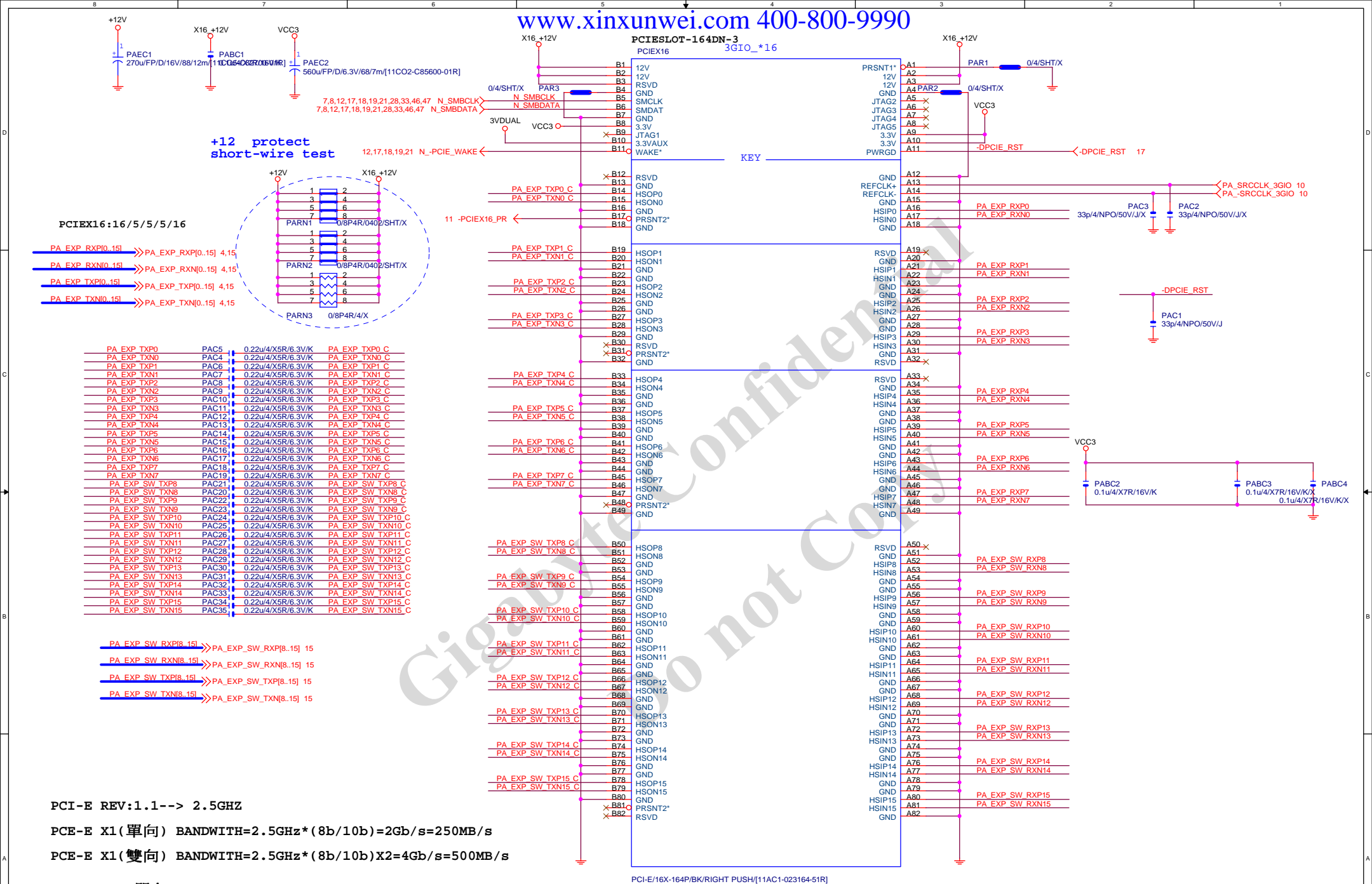
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xI--> xOa	L
xI--> xOb	H



PA\_EXP\_SW\_RXP[8..15] >> PA\_EXP\_SW\_RXP[8..15] 16  
 PA\_EXP\_SW\_RXN[8..15] >> PA\_EXP\_SW\_RXN[8..15] 16  
 PA\_EXP\_SW\_TXP[8..15] >> PA\_EXP\_SW\_TXP[8..15] 16  
 PA\_EXP\_SW\_TXN[8..15] >> PA\_EXP\_SW\_TXN[8..15] 16

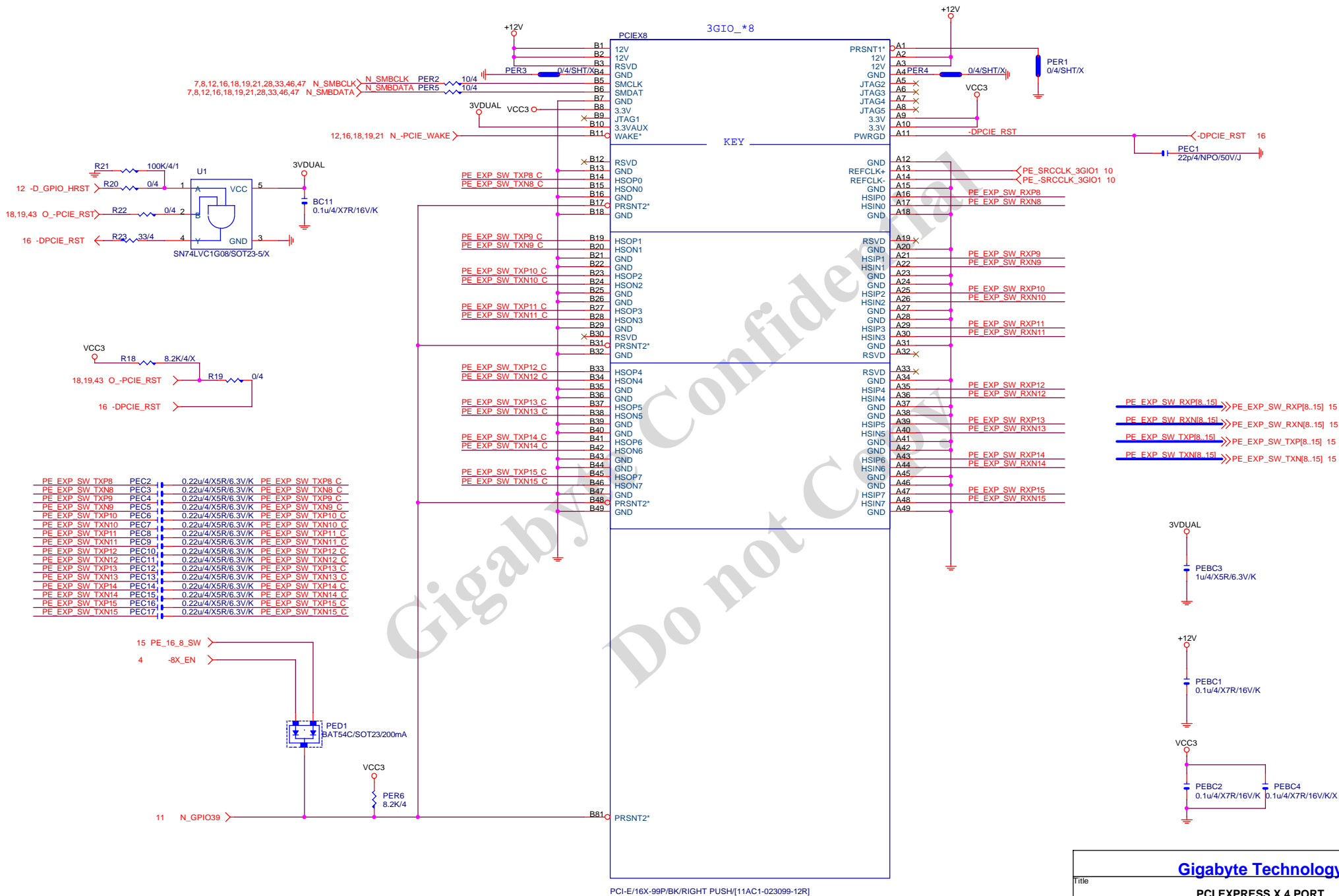
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 PE\_EXP\_SW\_TXN[8..15] >> PE\_EXP\_SW\_TXN[8..15] 17

PA\_EXP\_RXP[0..15] >> PA\_EXP\_RXP[0..15] 4,16  
 PA\_EXP\_RXN[0..15] >> PA\_EXP\_RXN[0..15] 4,16  
 PA\_EXP\_TXP[0..15] >> PA\_EXP\_TXP[0..15] 4,16  
 PA\_EXP\_TXN[0..15] >> PA\_EXP\_TXN[0..15] 4,16



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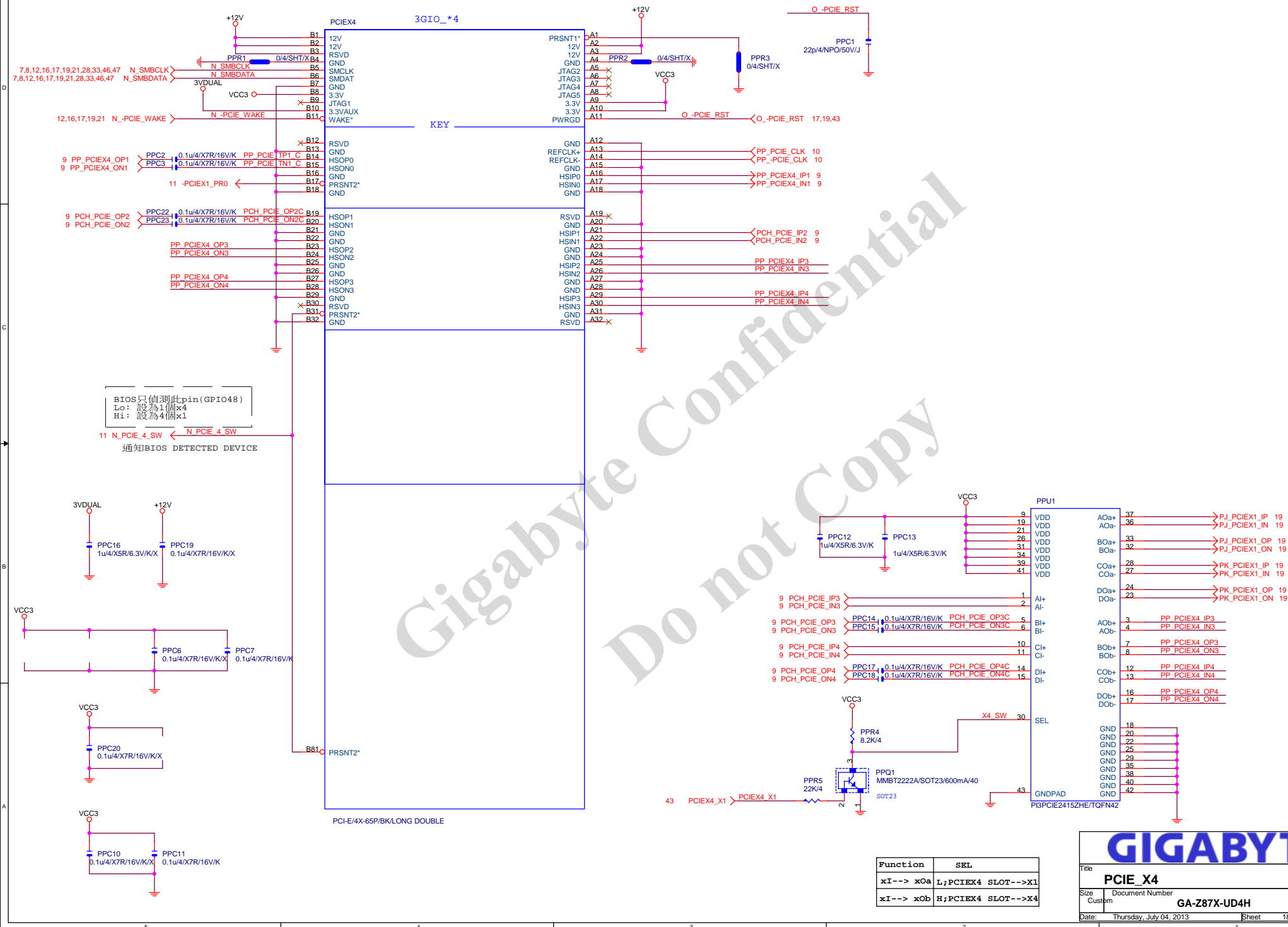
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PCI EXPRESS * 16			
Size	Document Number	Rev	
Custom	GA-Z87X-UD4H	1.1	
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Title			PCI EXPRESS X 4 PORT	
Size	Document Number		Rev	
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PCIE\*4

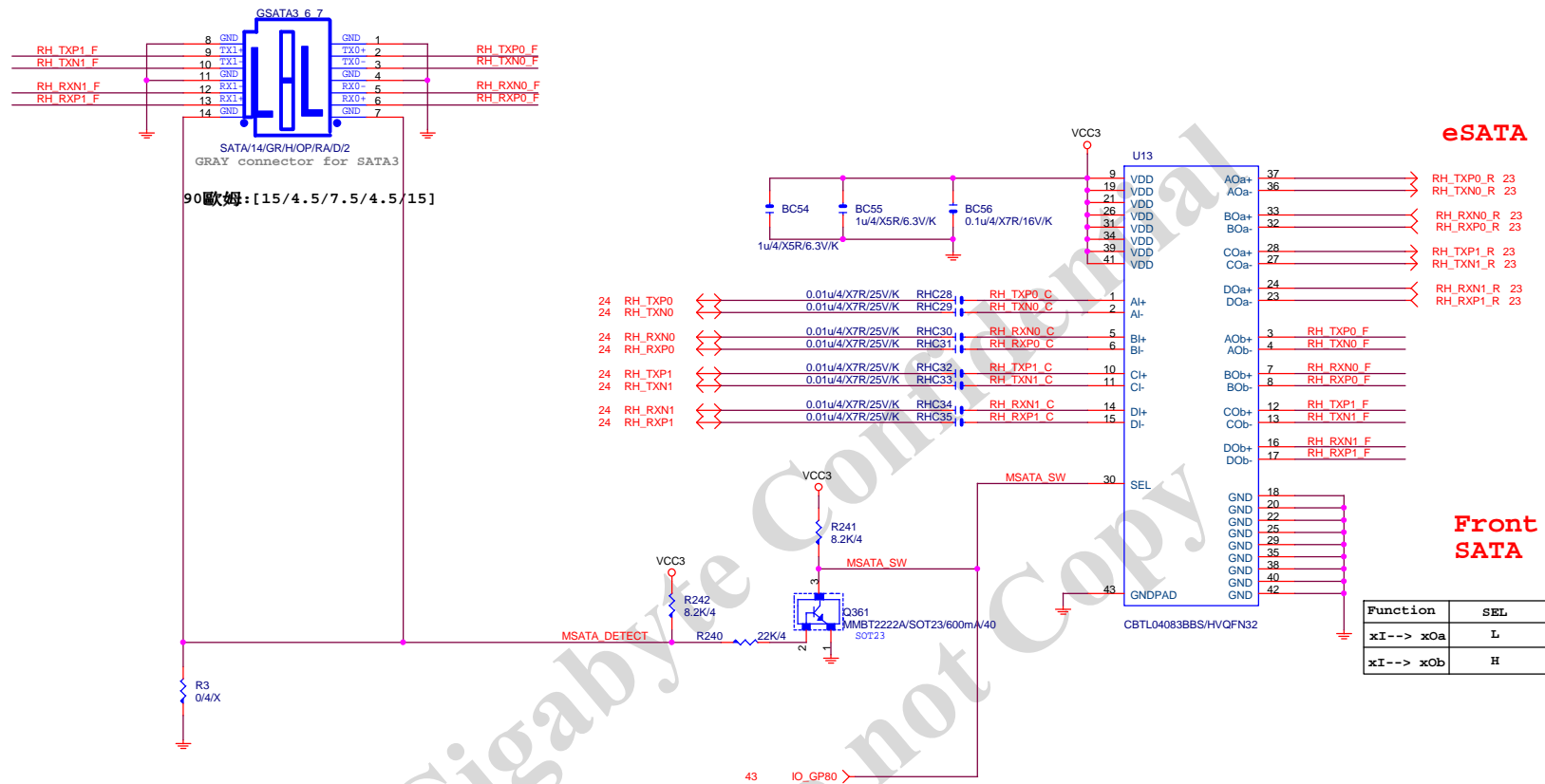










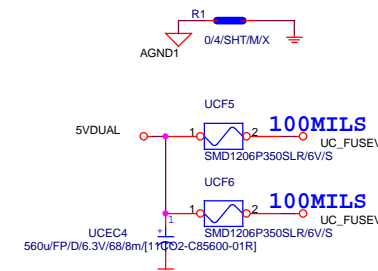
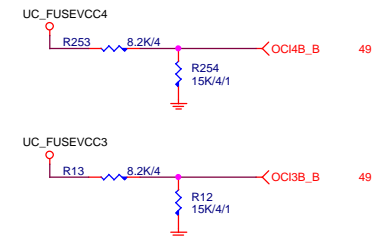
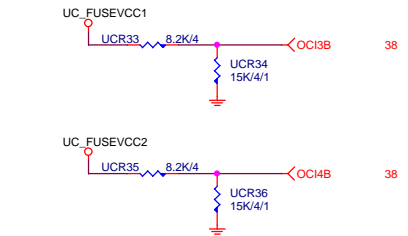
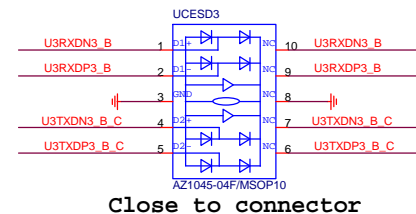
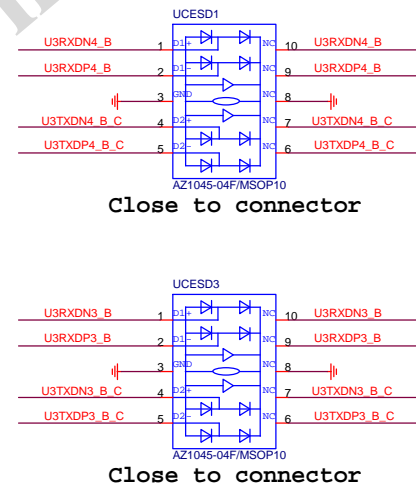
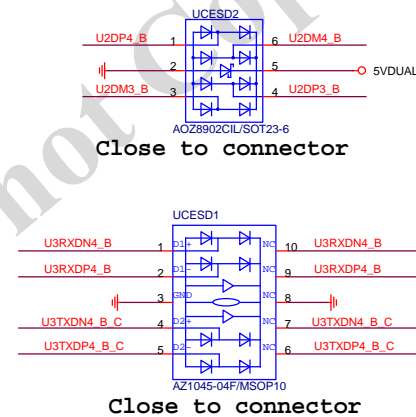
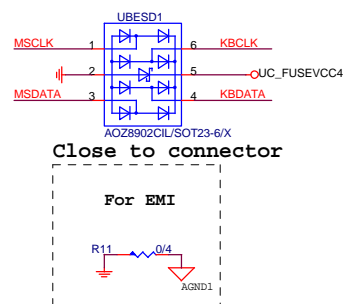
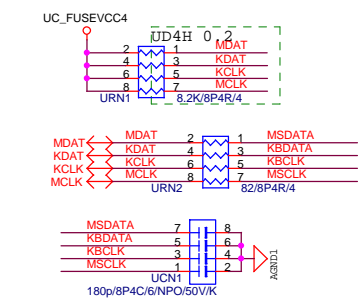
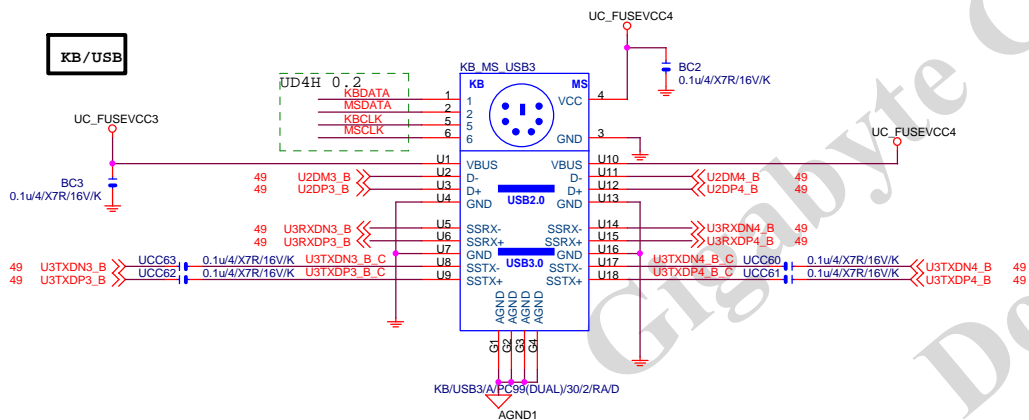
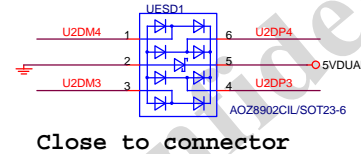
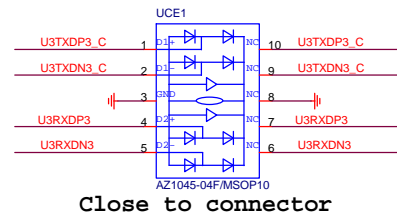
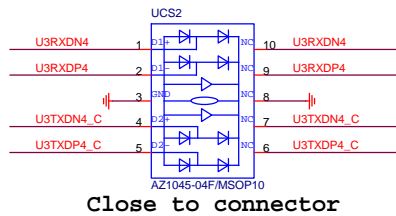
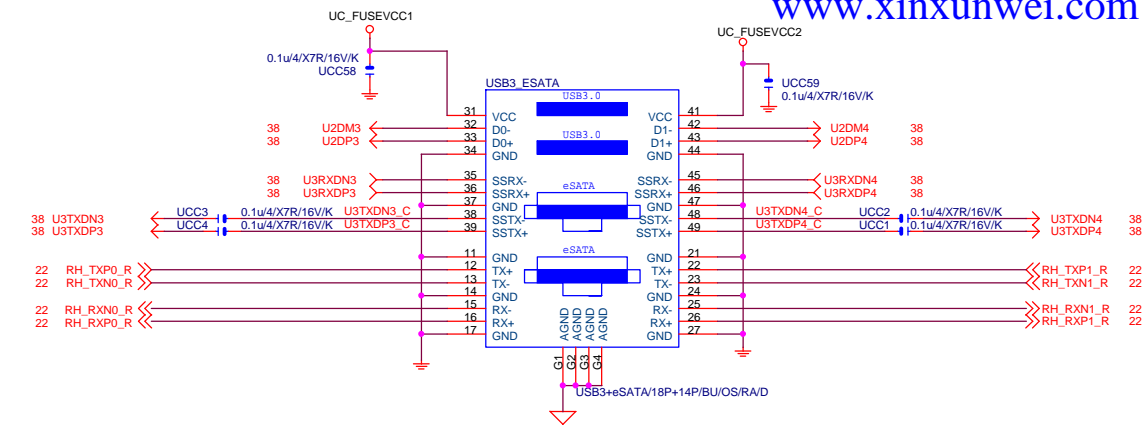


# GIGABYTE™

Title  
**SATA SWITCH, GSATA3\_67**

Size Custom Document Number **GA-Z87X-UD4H** Rev **1.1**

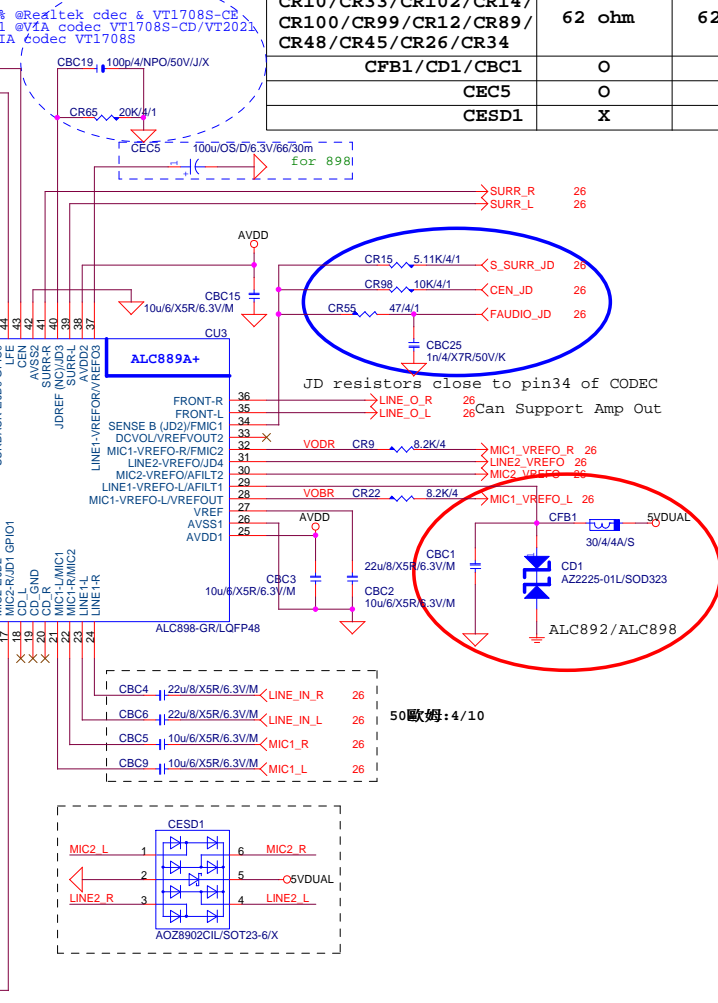
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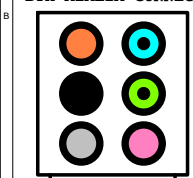
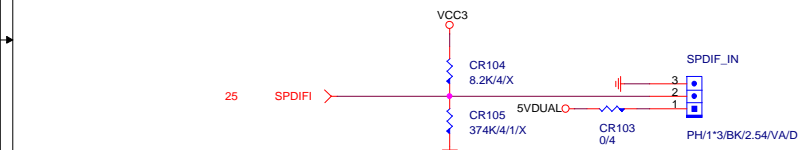
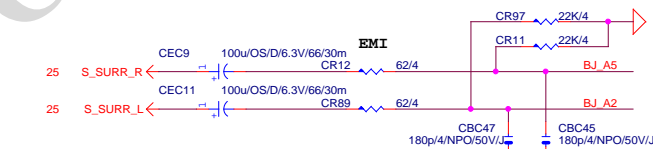
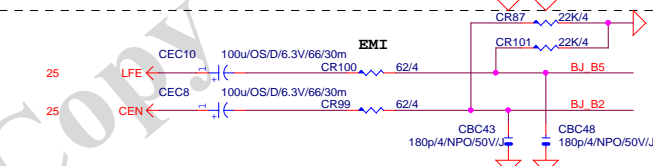
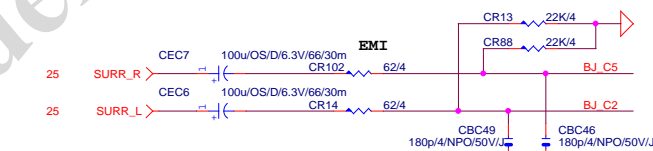
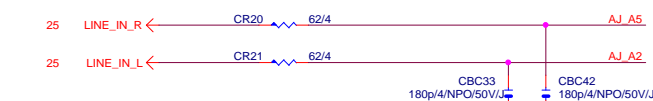




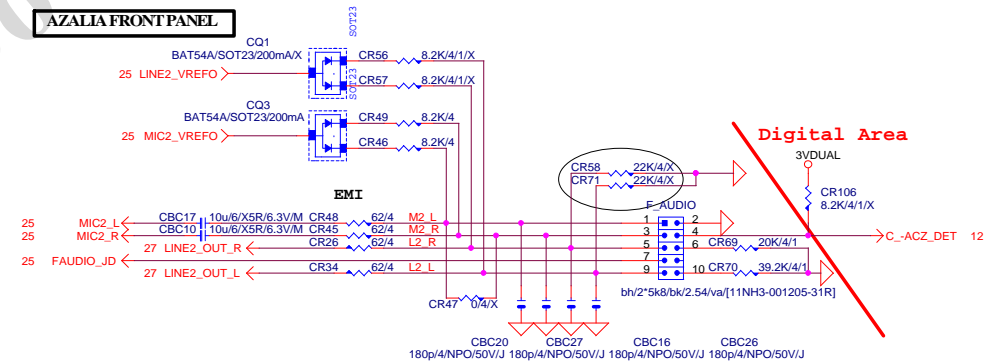
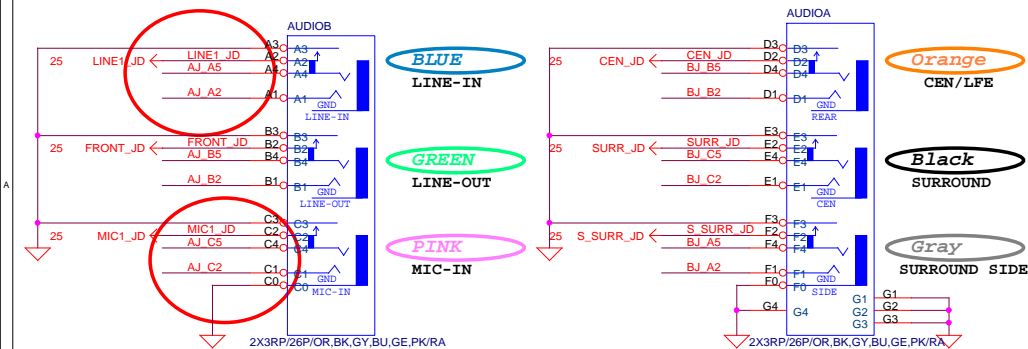


VT2021
O
X
470hm+1nF
O
X
10uF/X5R
5.1K/4/1
3.3K/4/1
X
10K/4/1
75 ohm
X
X
X





11NR6-403007-21R

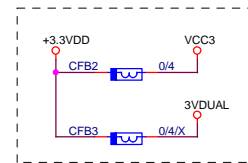
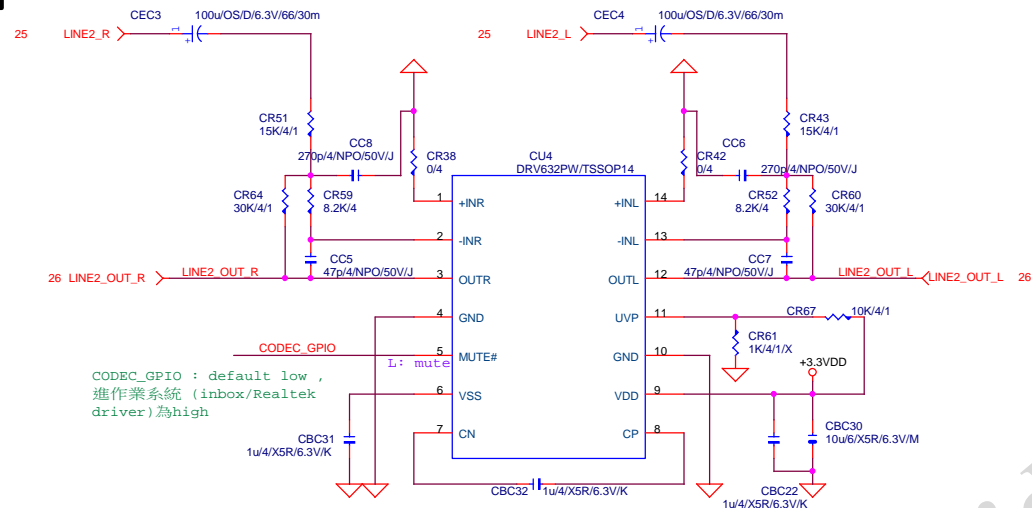


## Digital Area

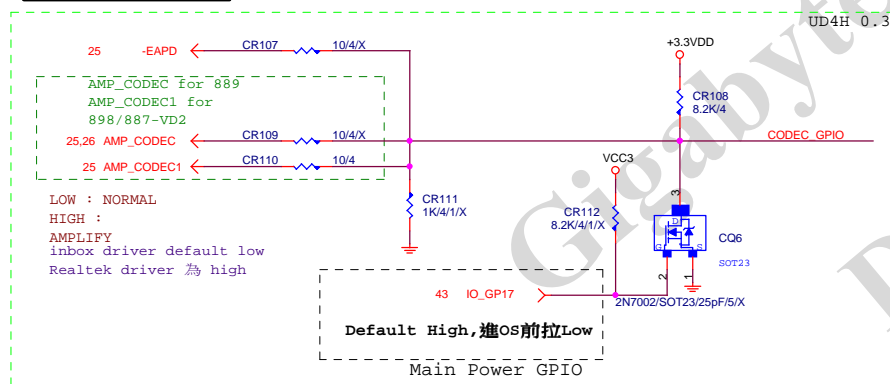
## Gigabyte Technology

Title				<b>AUDIO JACK</b>			
Size Custom		Document Number				Rev	
		<b>GA-Z87X-UD4H</b>				<b>1.</b>	
Date:		Thursday, July 04, 2013			Sheet		26 of 51

## HEADPHONE



## For Pop Noise

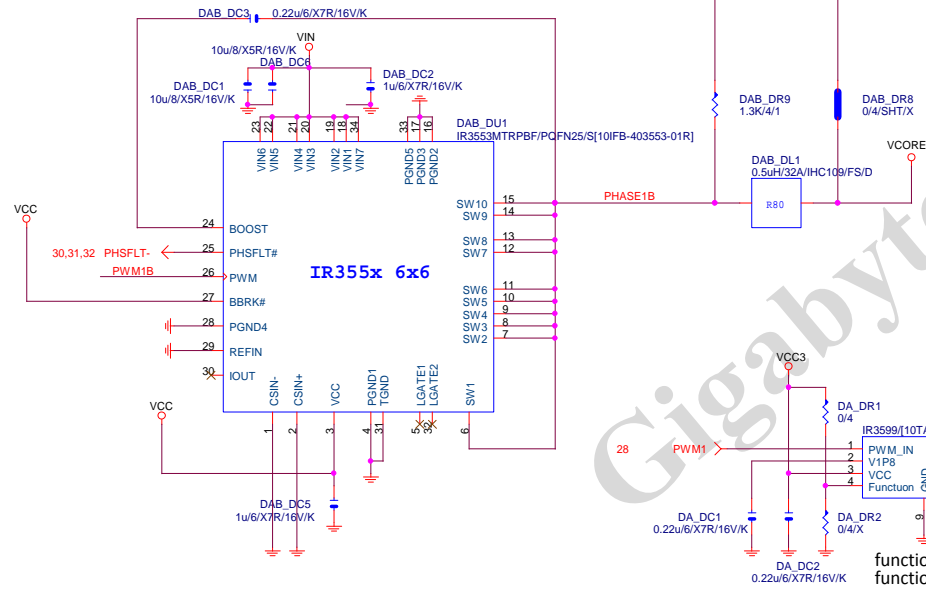
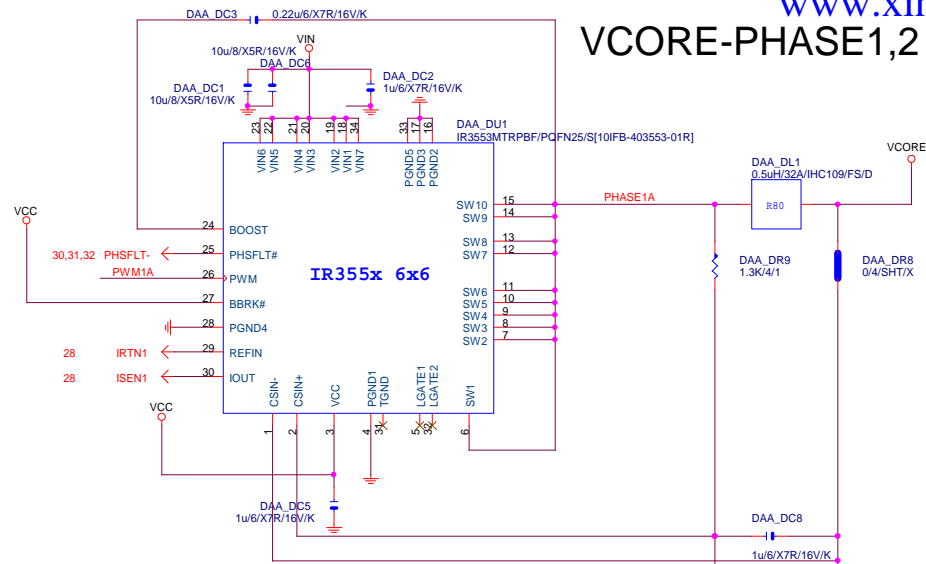


Gigabyte Technology

Title			8-CH DAC & Anti-Pop / Mute
Size	Document Number	GA-Z87X-UD4H	
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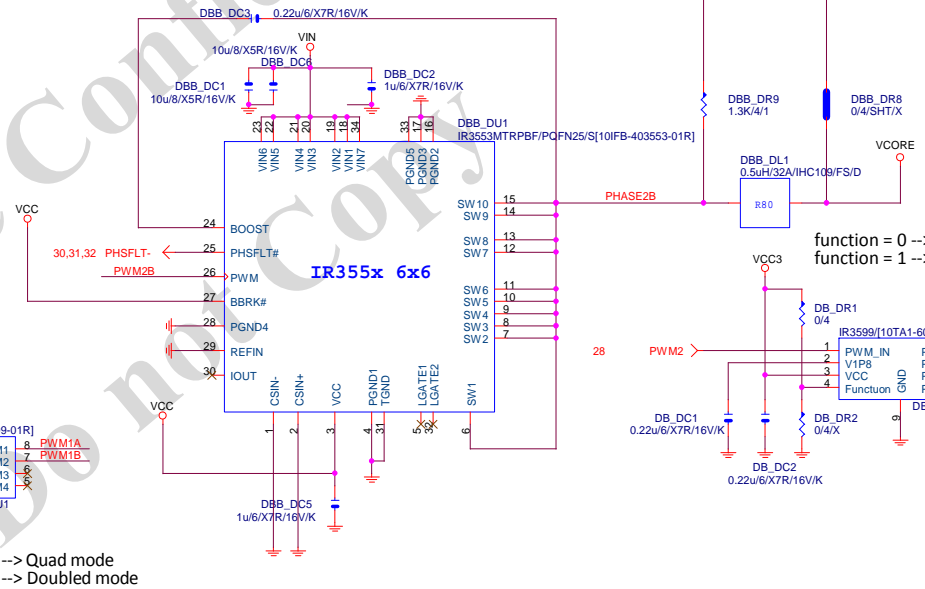
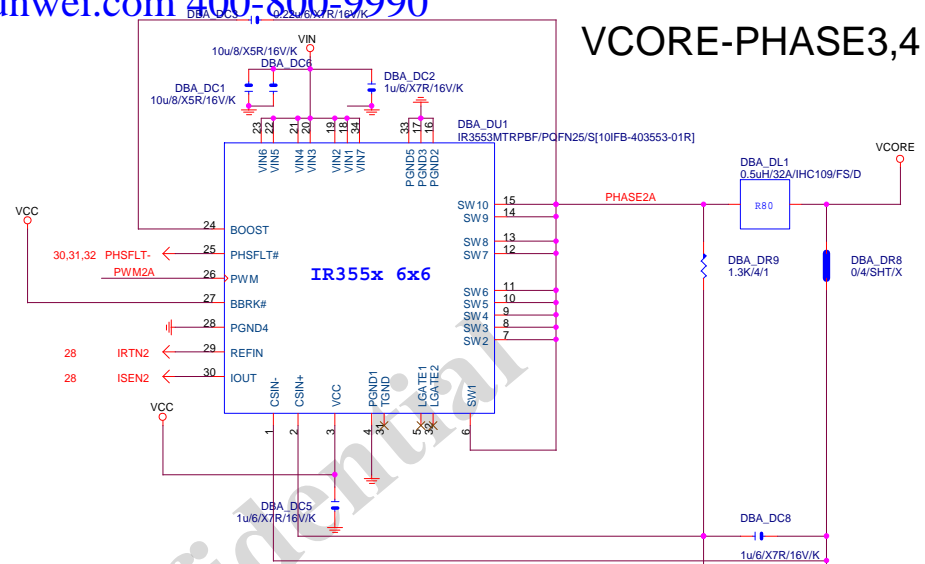
## VCORE-PHASE1,2



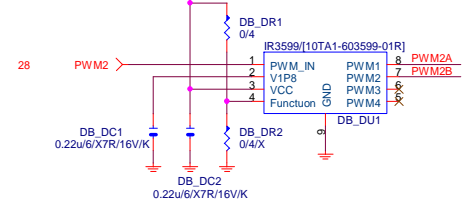
Add MOSFET Anti-Burn  
 4.11.28 N\_THRMTRIP ← DAR109 0/4 PHSFLT- UD4H 0.3 VCC  
 6.2K/4/X

function = 0 --> Quad mode  
 function = 1 --> Doubled mode

## VCORE-PHASE3,4



function = 0 --> Quad mode  
 function = 1 --> Doubled mode

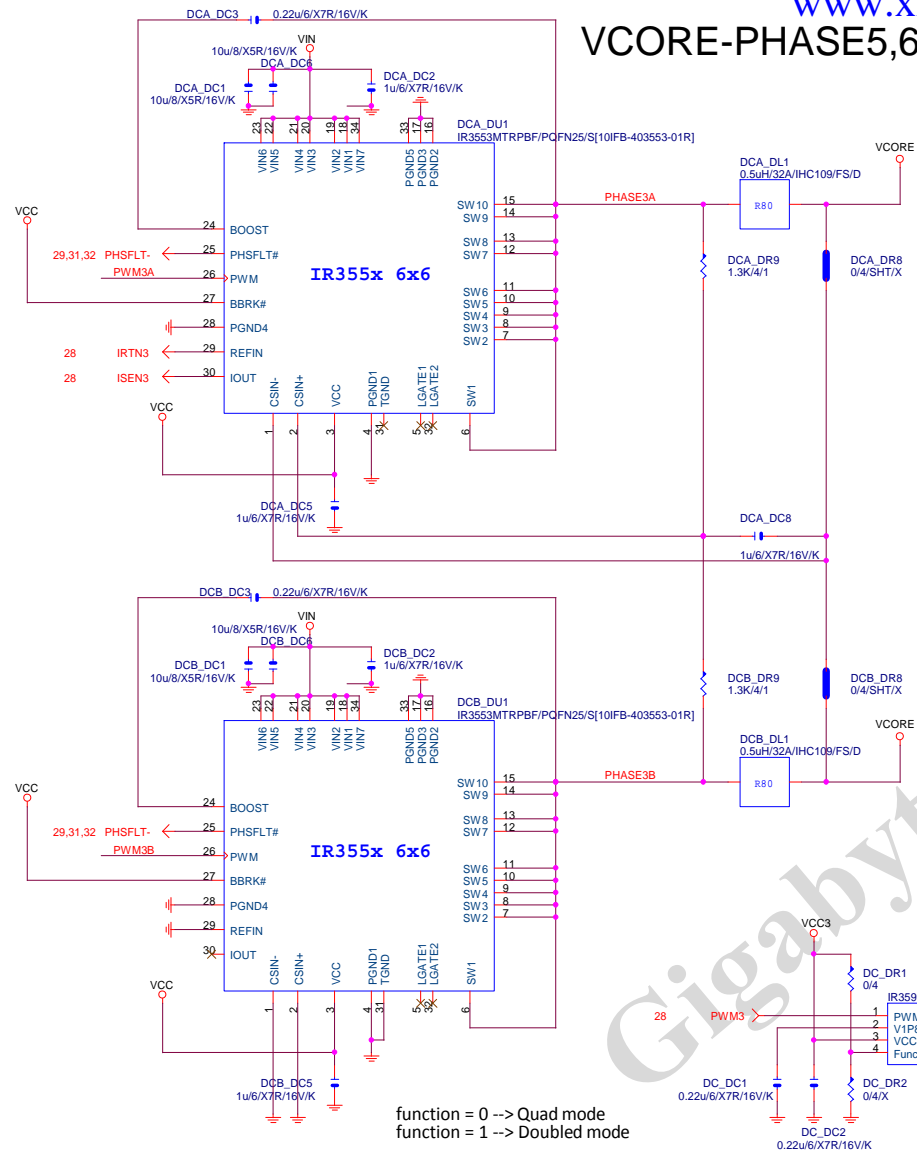


Gigabyte Technology

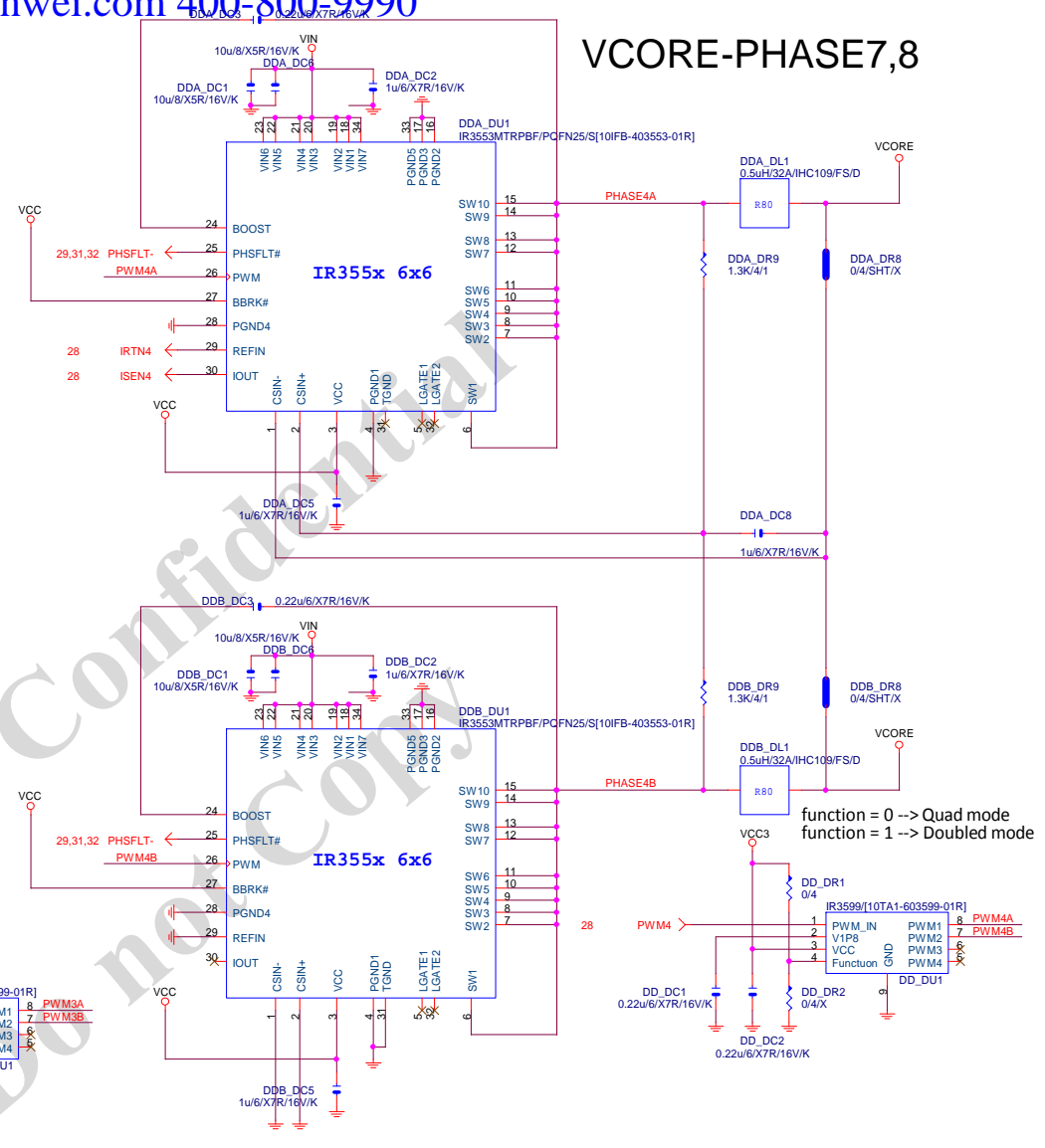
Title			
CPU CORE VR			
Size	Document Number	Rev	
Custom	GA-Z87X-UD4H	1.1	
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## VCORE-PHASE5,6



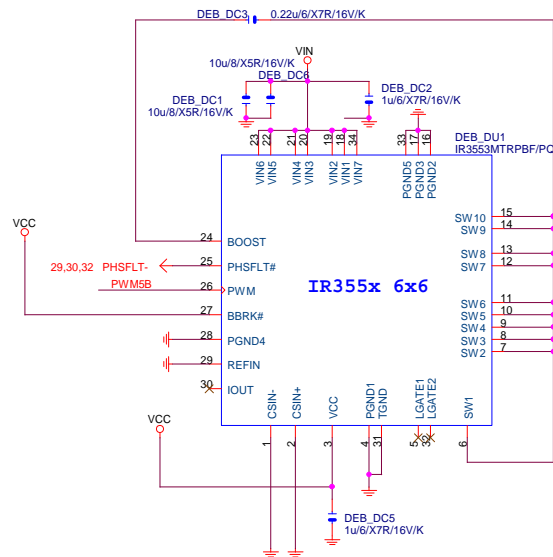
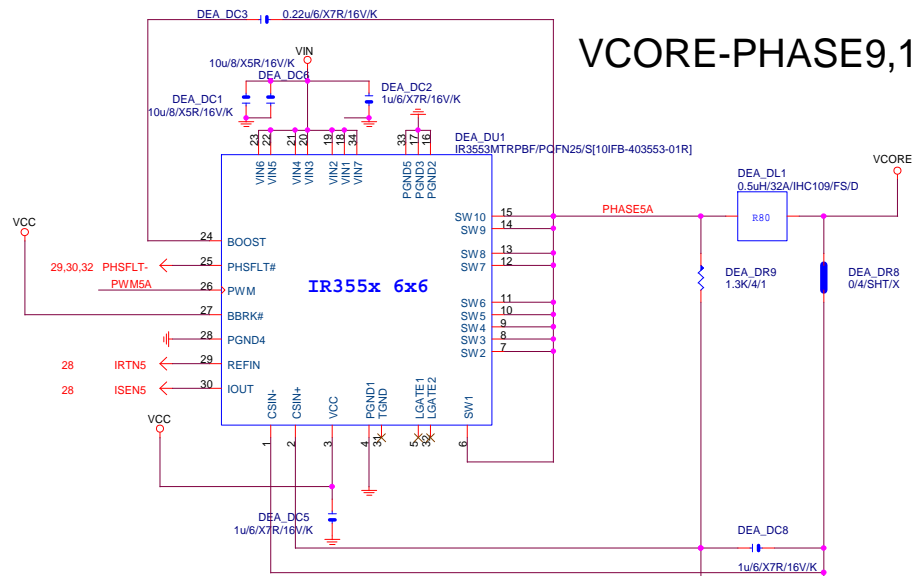
## VCORE-PHASE7,8



Gigabyte Technology

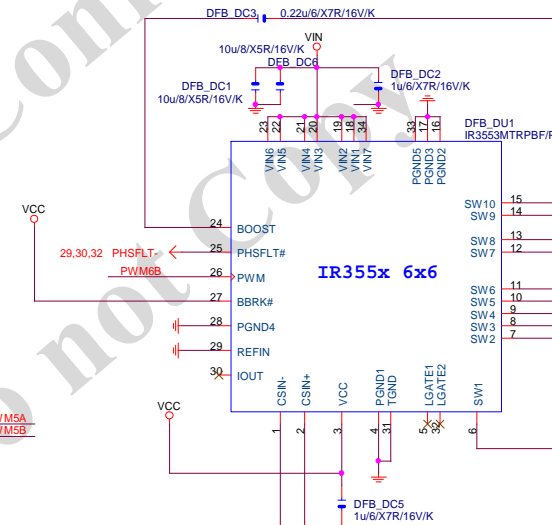
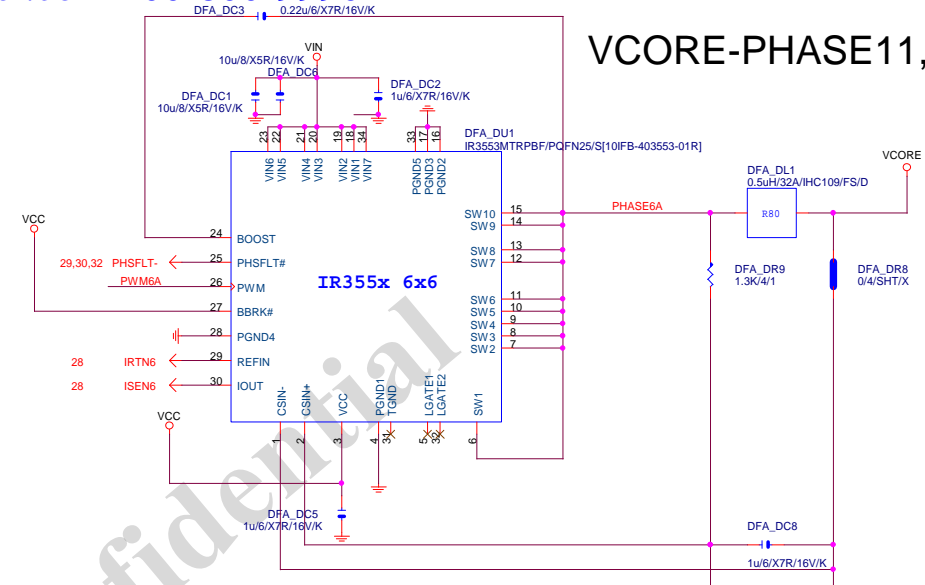
Title			
CPU CORE VR			
Size	Document Number	Rev	
Custom	GA-Z87X-UD4H	1.1	
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## VCORE-PHASE9,10

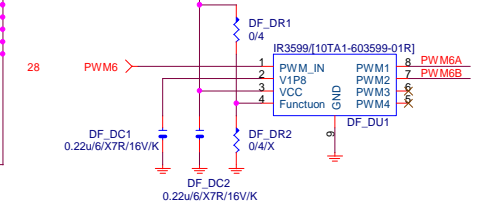


function = 0 --> Quad mode  
function = 1 --> Doubled mode

## VCORE-PHASE11,12



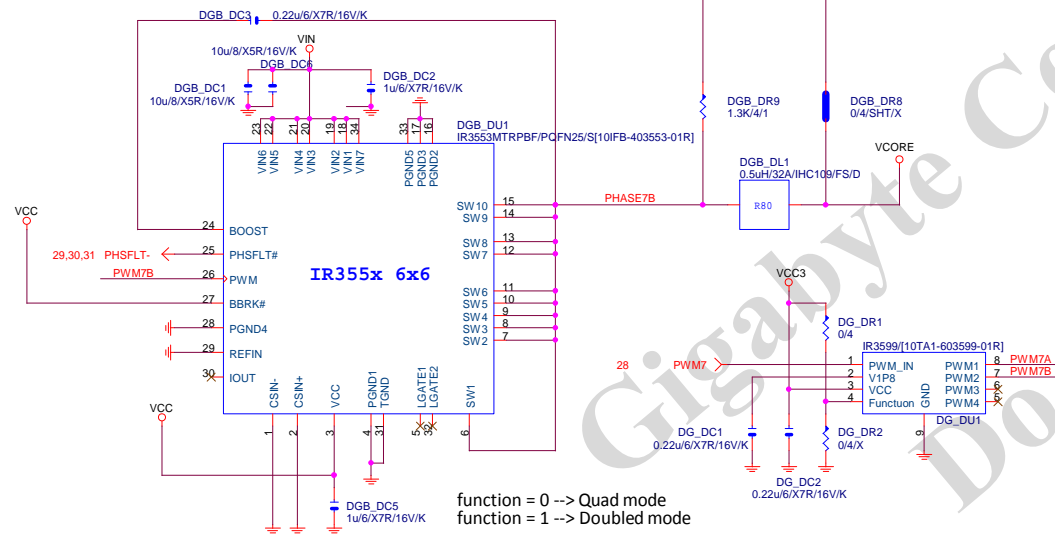
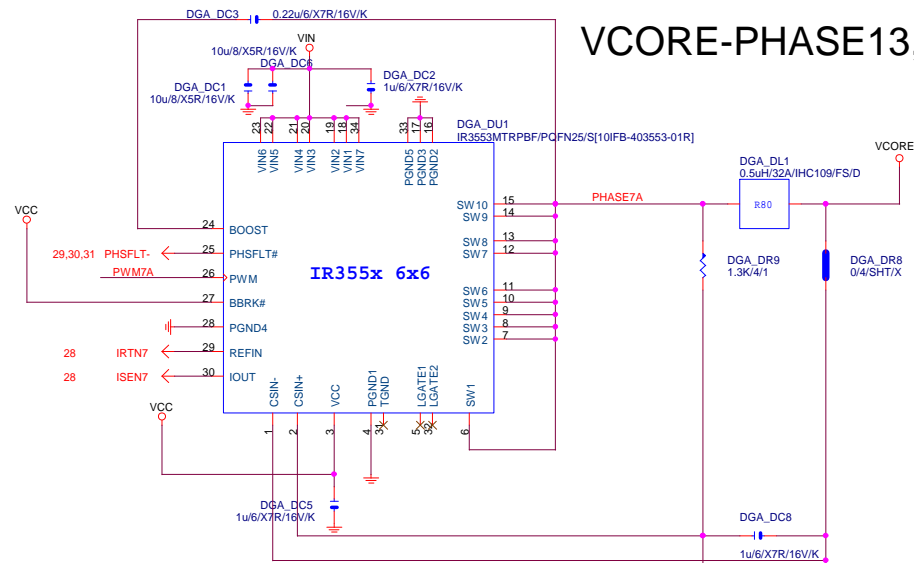
function = 0 --> Quad mode  
function = 1 --> Doubled mode



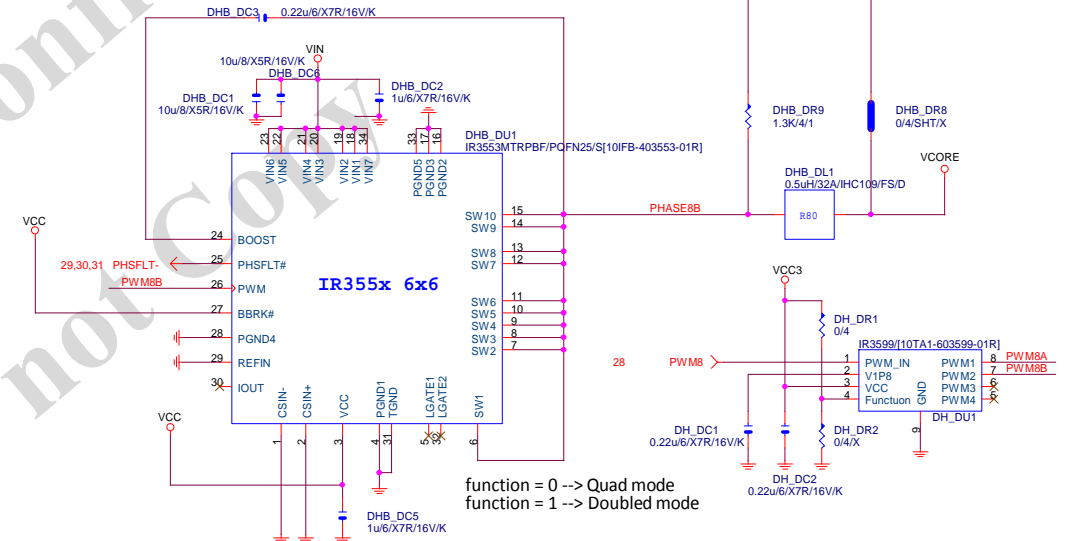
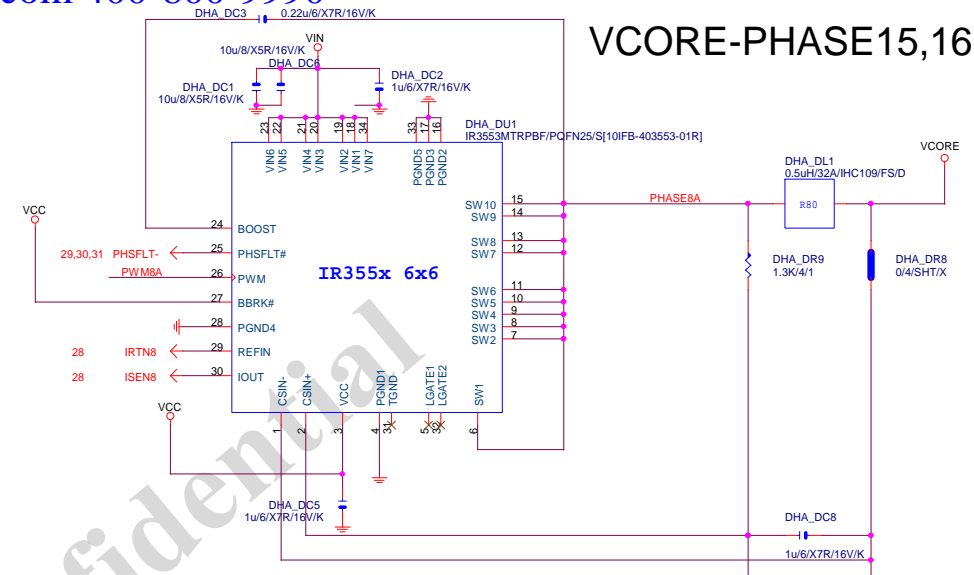
Gigabyte Technology

Title			IR3553-VCORE
Size	Document Number	GA-Z87X-UD4H	
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## VCORE-PHASE13,14

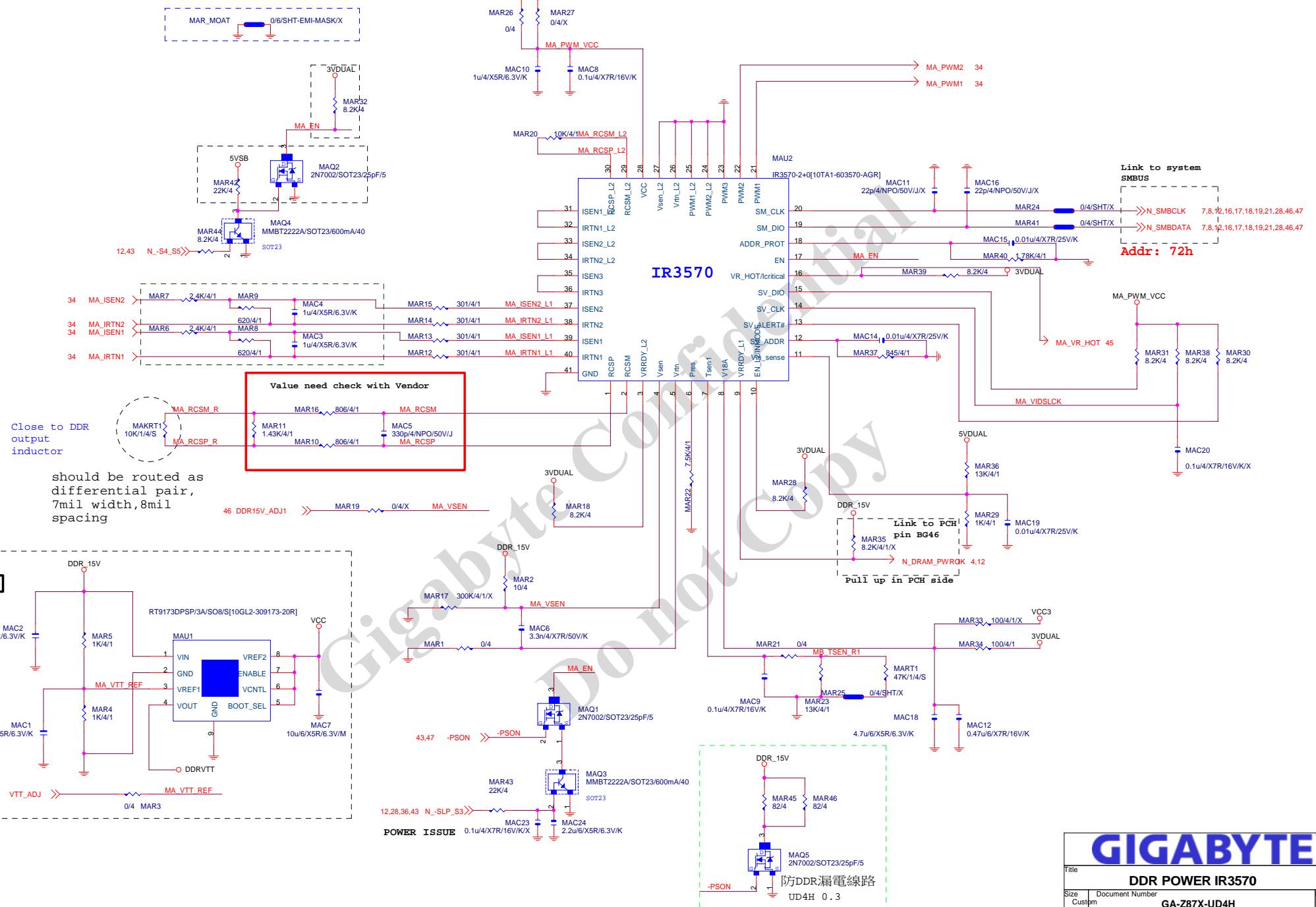


## VCORE-PHASE15,16

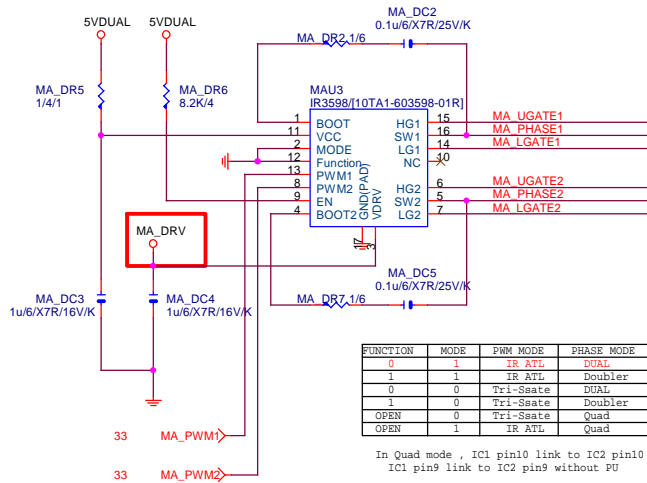


Gigabyte Technology

Title		IR3553-VCORE	
Size	Document Number	GA-Z87X-UD4H	
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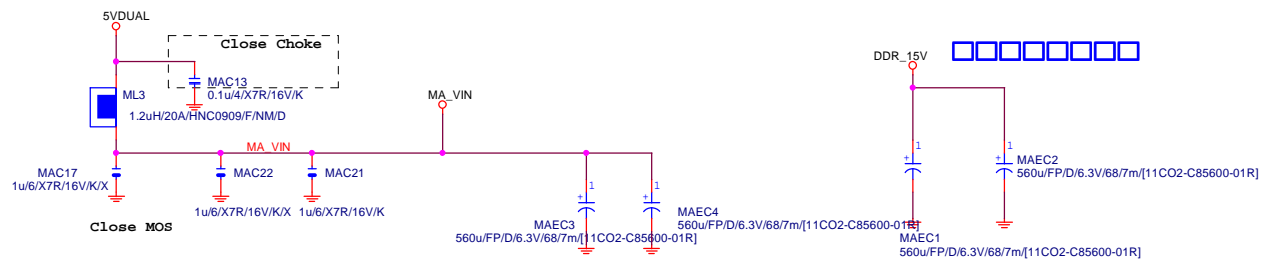
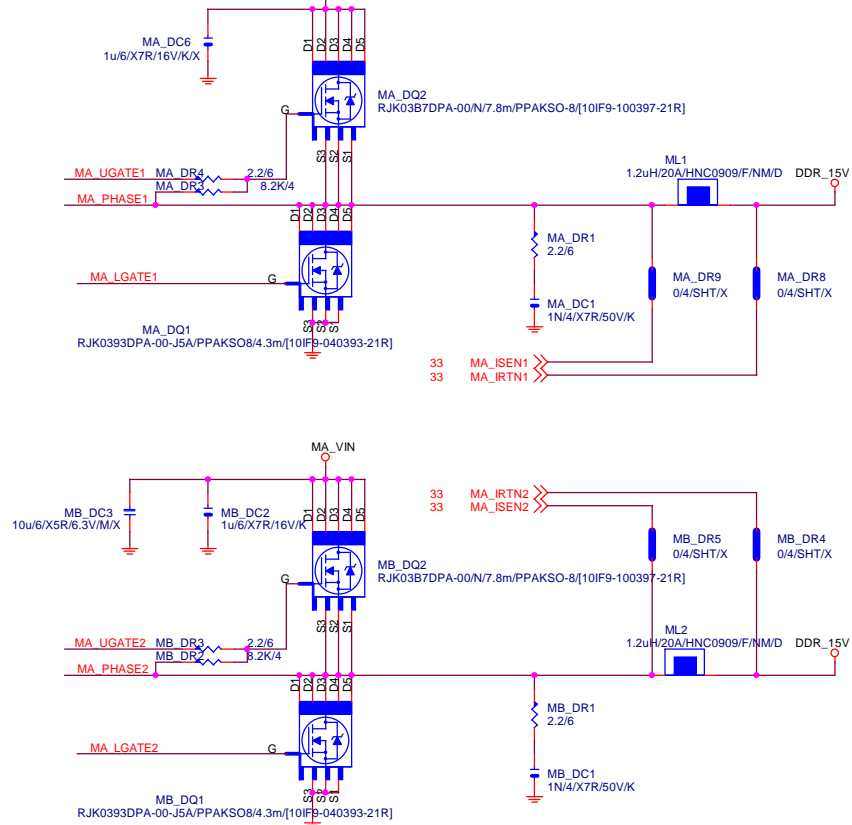
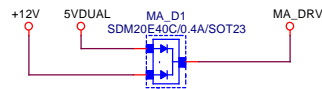


DDR\_15V



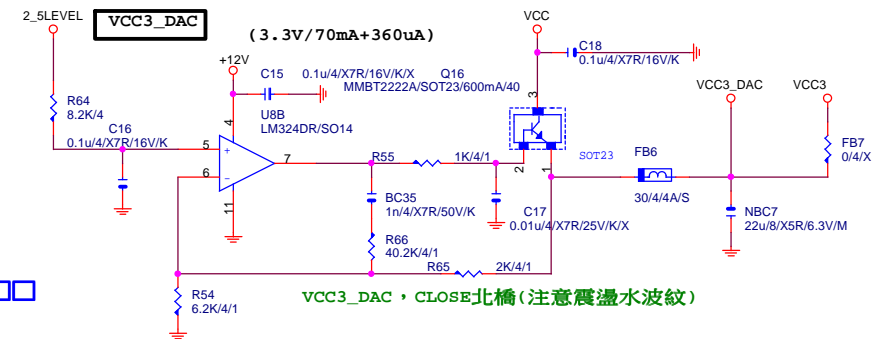
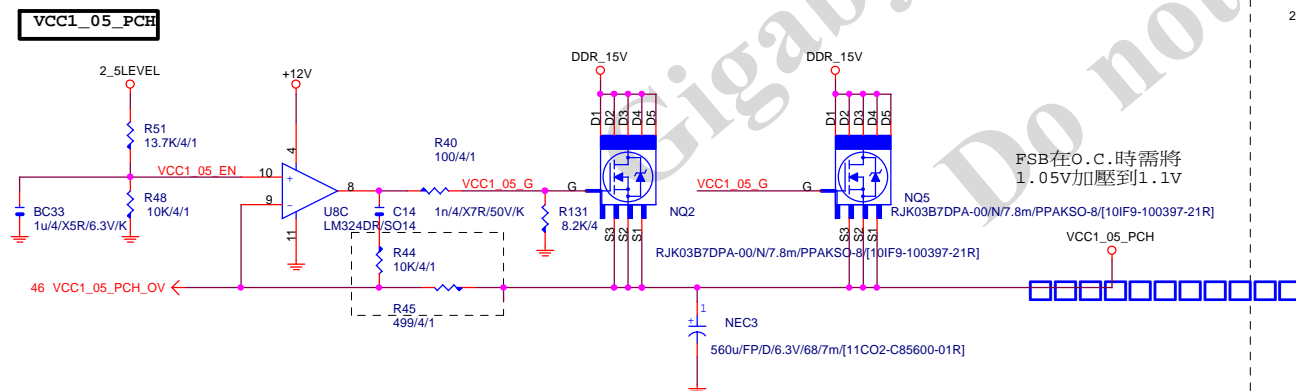
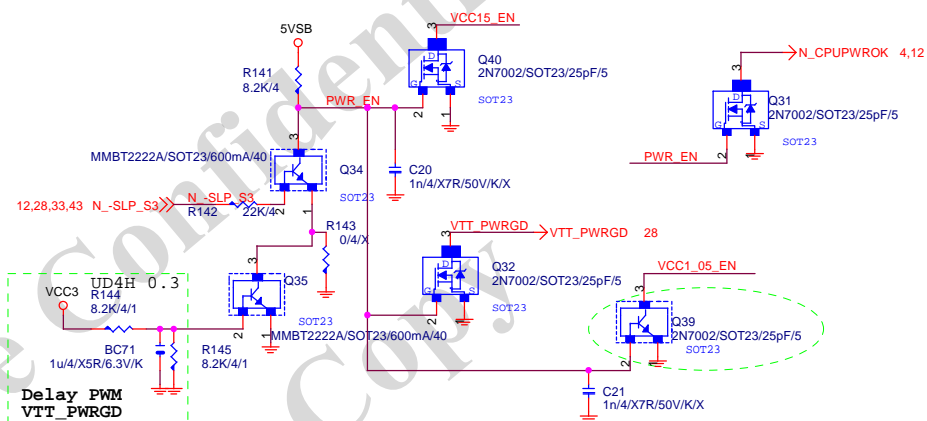
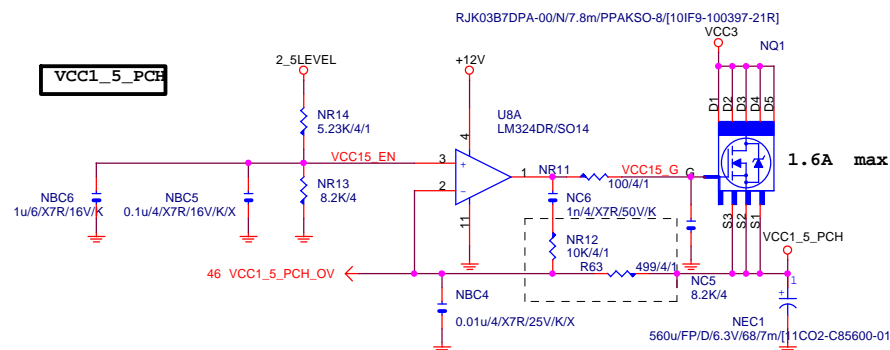
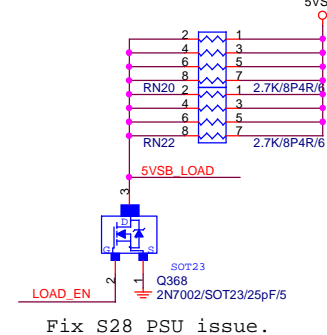
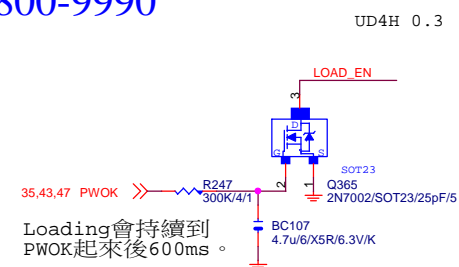
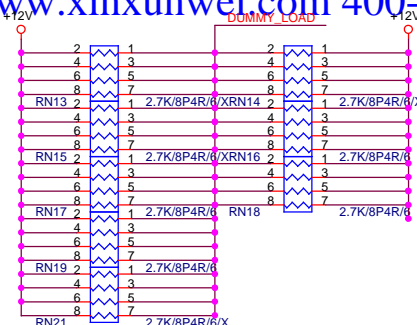
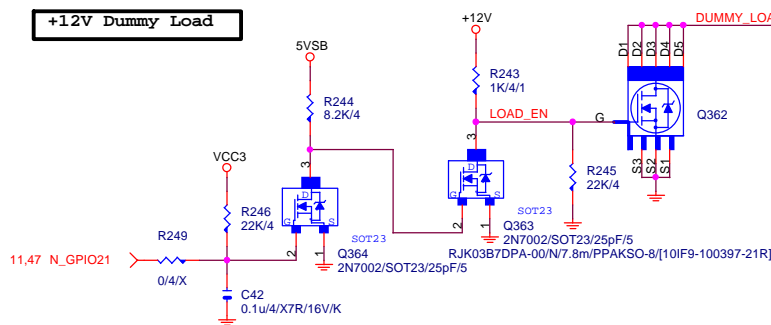
FUNCTION	MODE	PWM MODE	PHASE MODE
0	1	IR ATL	DUAL
1	1	IR ATL	Doubler
0	0	Tri-Ssate	DUAL
1	0	Tri-Ssate	Doubler
OPEN	0	Tri-Ssate	Quad
OPEN	1	IR ATL	Quad

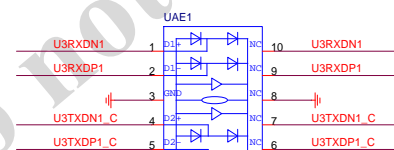
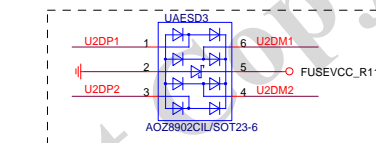
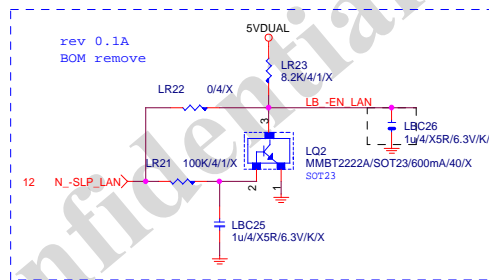
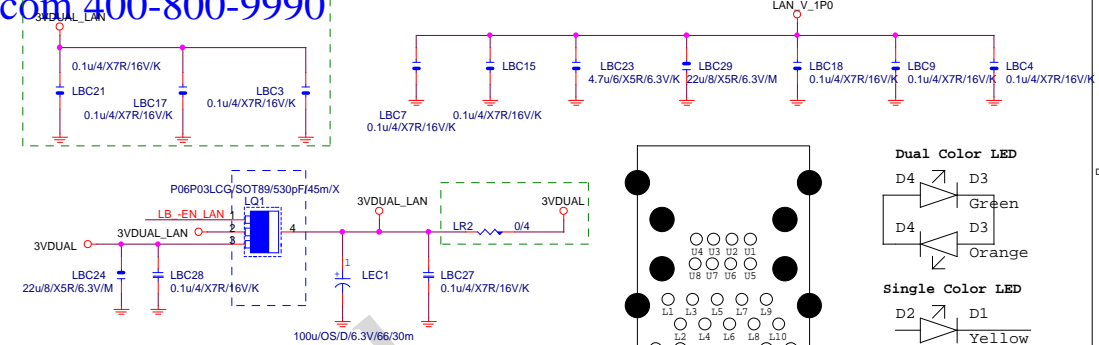
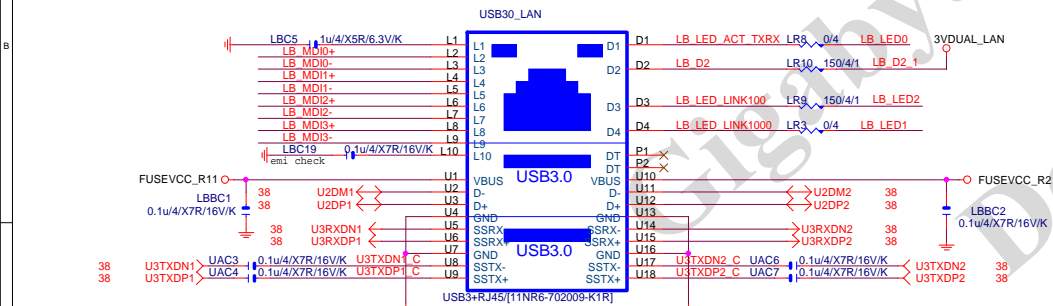
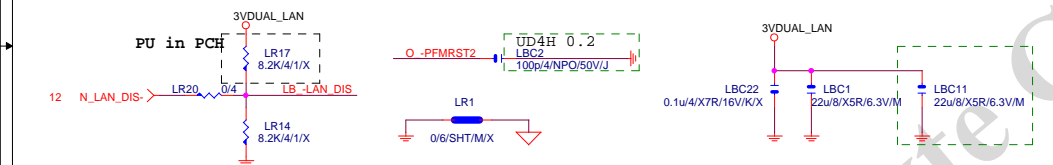
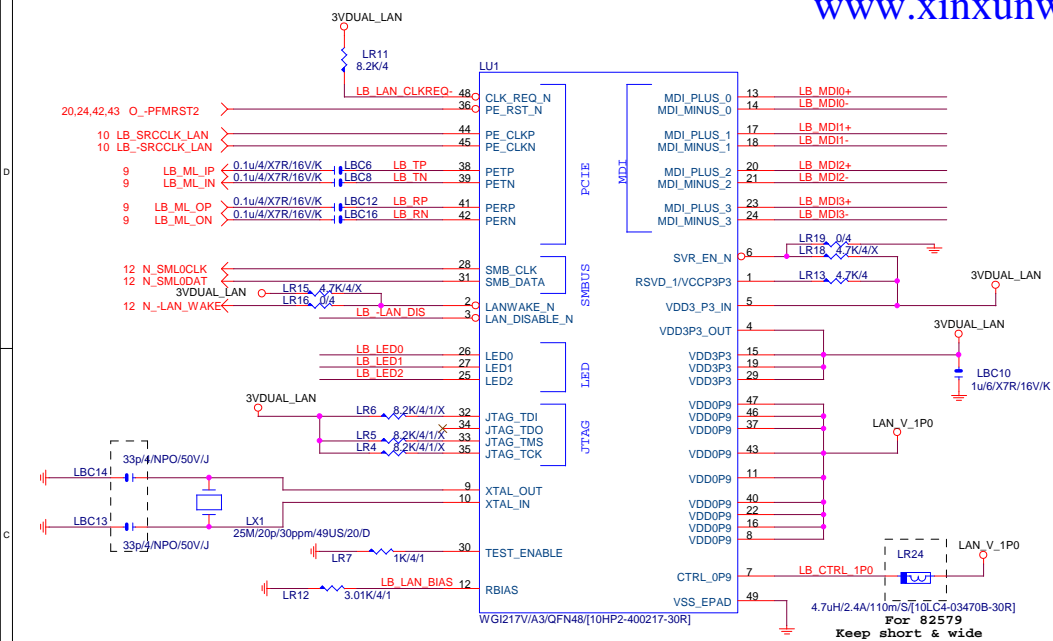
In Quad mode , IC1 pin10 link to IC2 pin10  
IC1 pin9 link to IC2 pin9 without PU



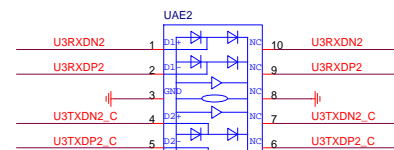




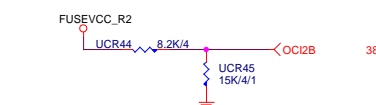
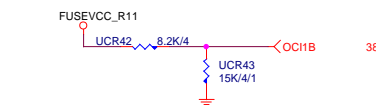
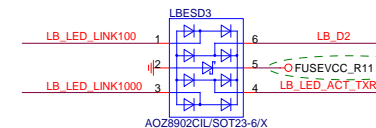
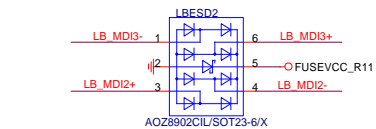
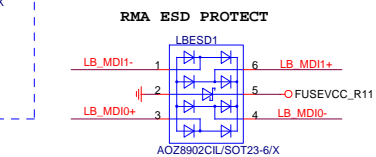
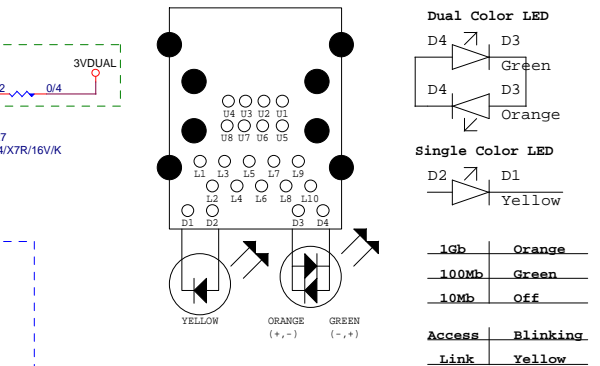




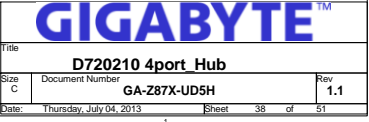
Close to connector

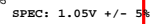
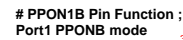
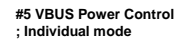
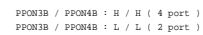
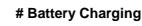


Close to connector

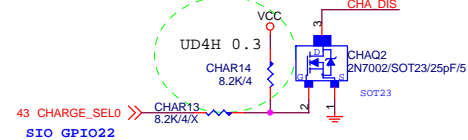
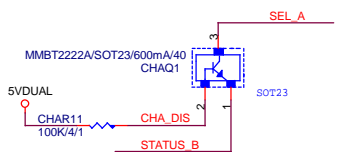
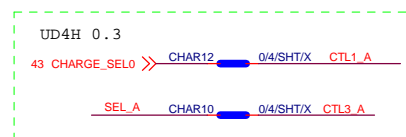
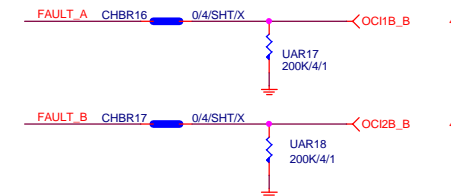
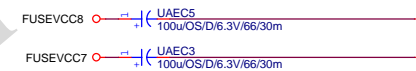
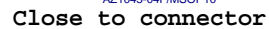


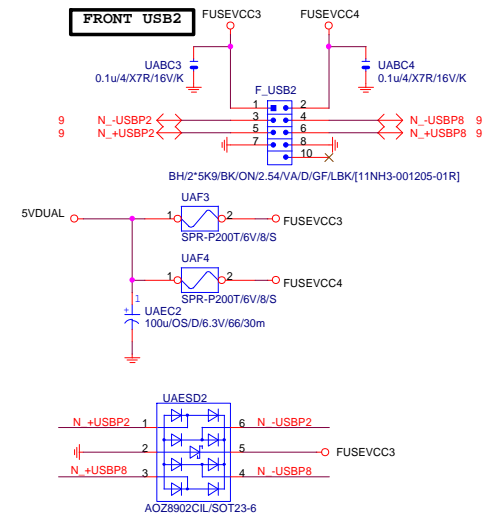
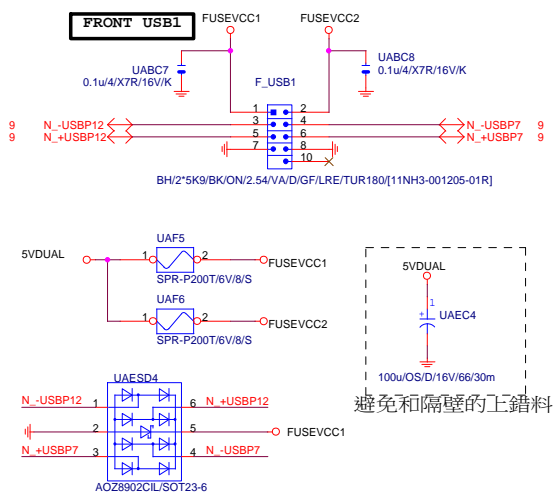
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INTEL LAN i217V			
Size	Document Number	Rev	
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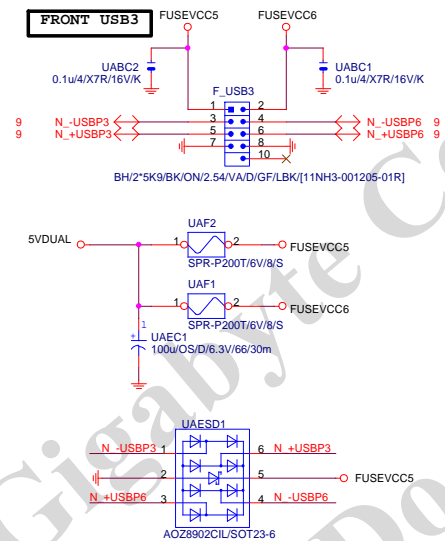
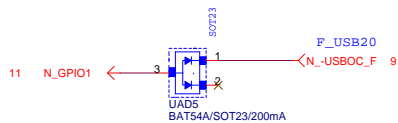


TI2543内建Fuse，移除UAFB1~2.

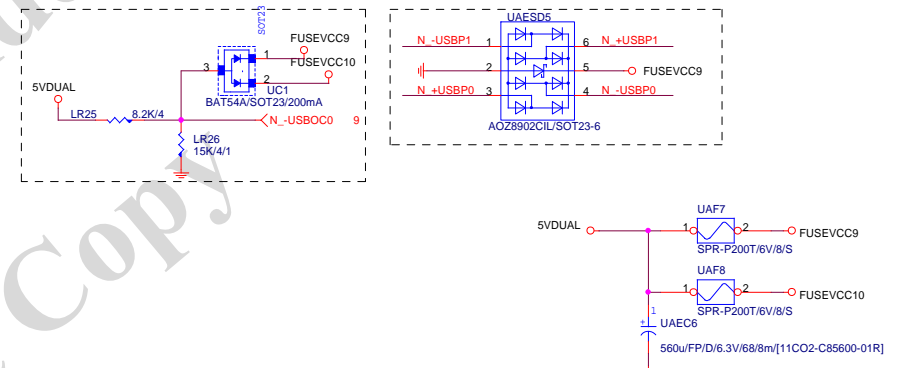
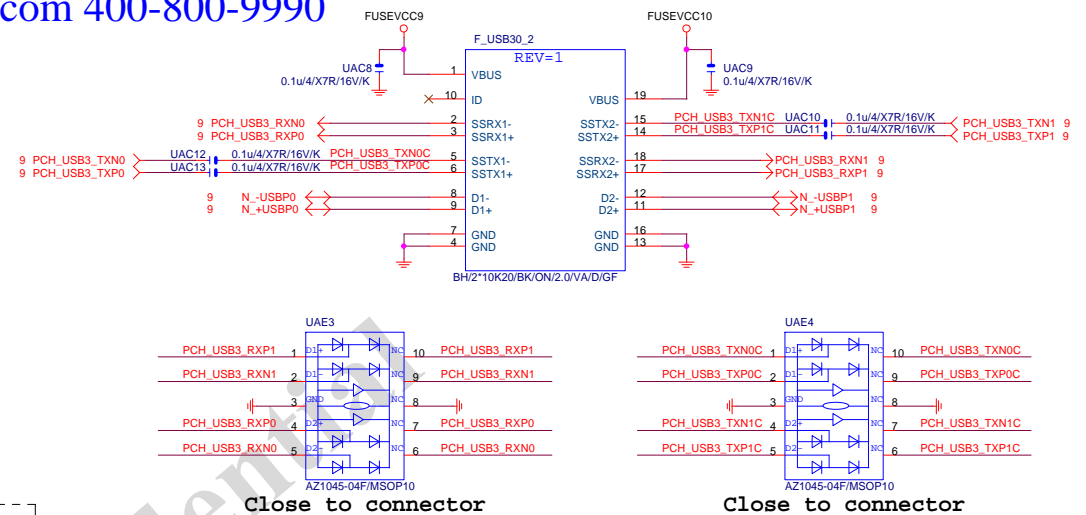
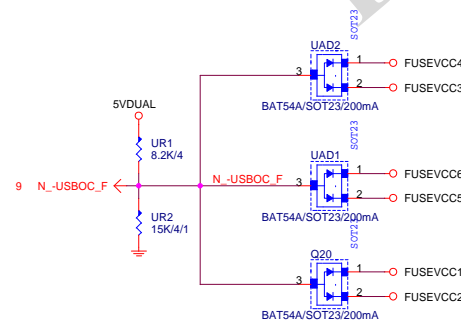




Close to connector



Close to connector



GIGABYTE™

FRONT USB 2.0, F_USB30_2		
Title	Document Number	Rev
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## MOSI For DMI RX Termination Voltage

12 N_ICH_SPI_MOSI	NR1	8.2K/4/1/X
12 N_ICH_SPI_CS	NR8	8.2K/4/1/X
12 N_ICH_SPI_CS	NR16	8.2K/4/1/X
-SPI_HOLD0	NR17	8.2K/4/1/X
-SPI_HOLD1	NR17	8.2K/4/1/X

-SPI_WP1	NR10	8.2K/4/1/X
-SPI_WP0	NR2	8.2K/4/1/X
N_ICH_SPI_MISO	NR9	8.2K/4/1/X

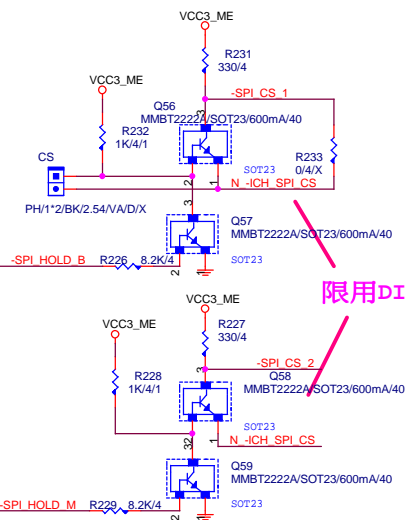
1 means floating  
0 means PD 1K

SPI MISO NR7 22/4 << N\_ICH\_SPI\_MISO 12

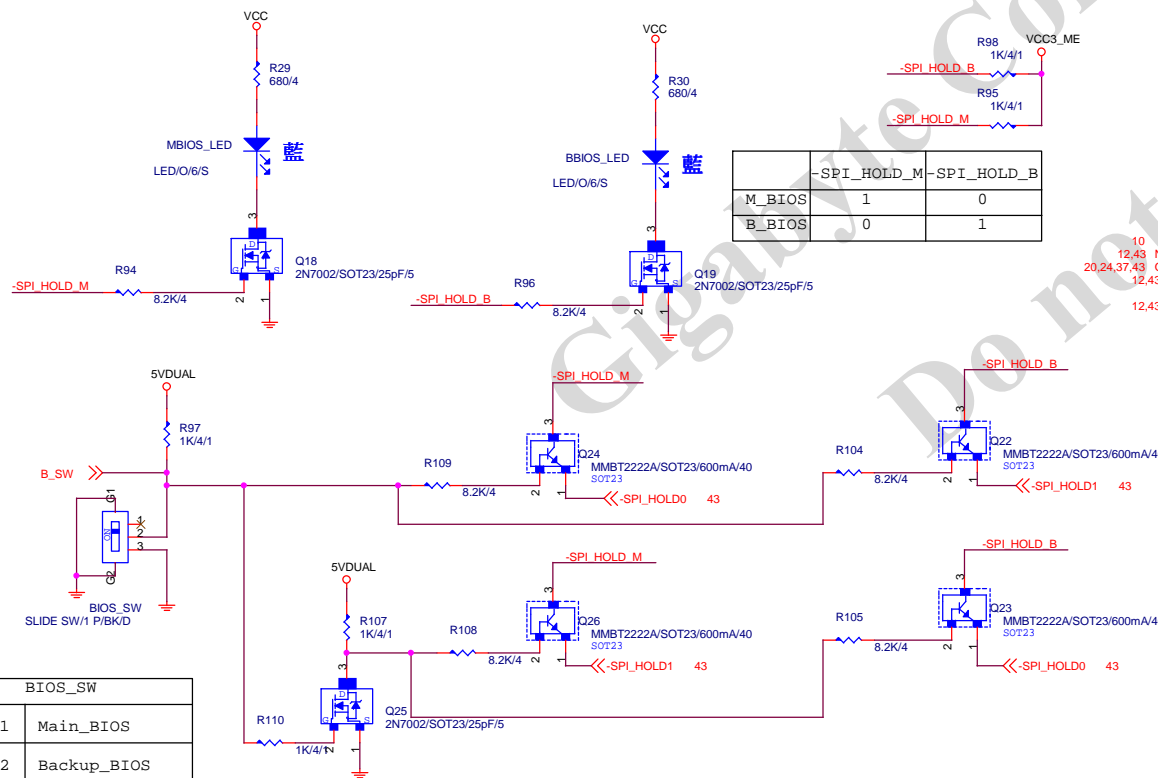
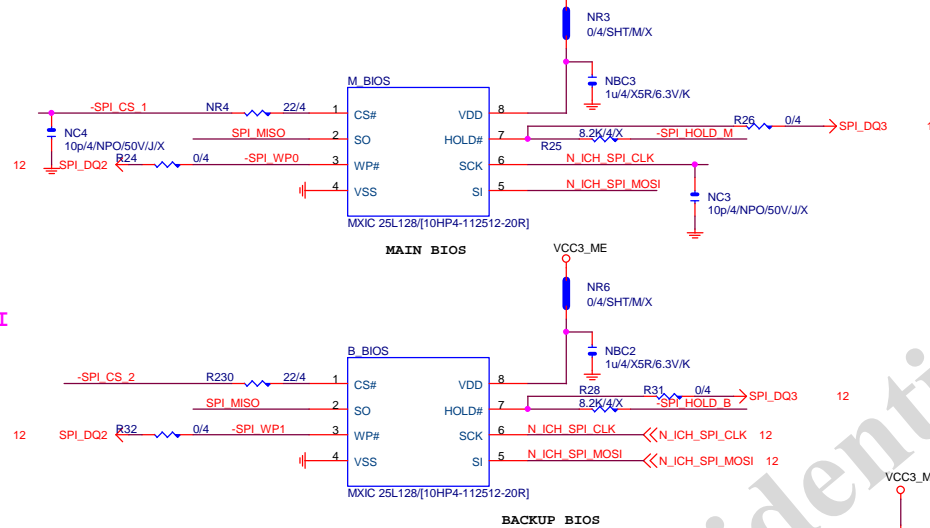
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

## SPI ROM

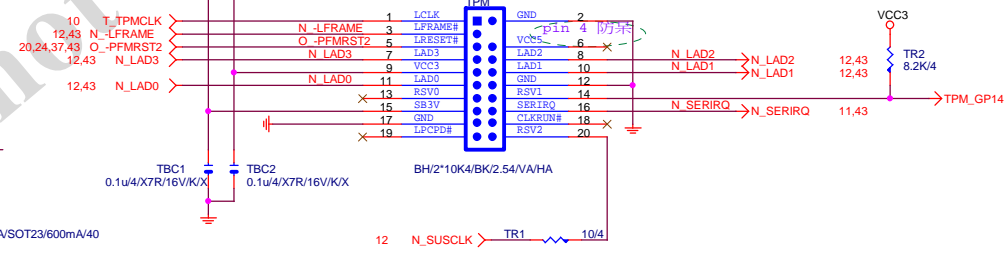
	Quad I/O	Traditional
Pin 2	SIO 1	S0
Pin 3	SIO 2	WP#
Pin 5	SIO 0	SI
Pin 7	SIO 3	HOLD#



限用DII

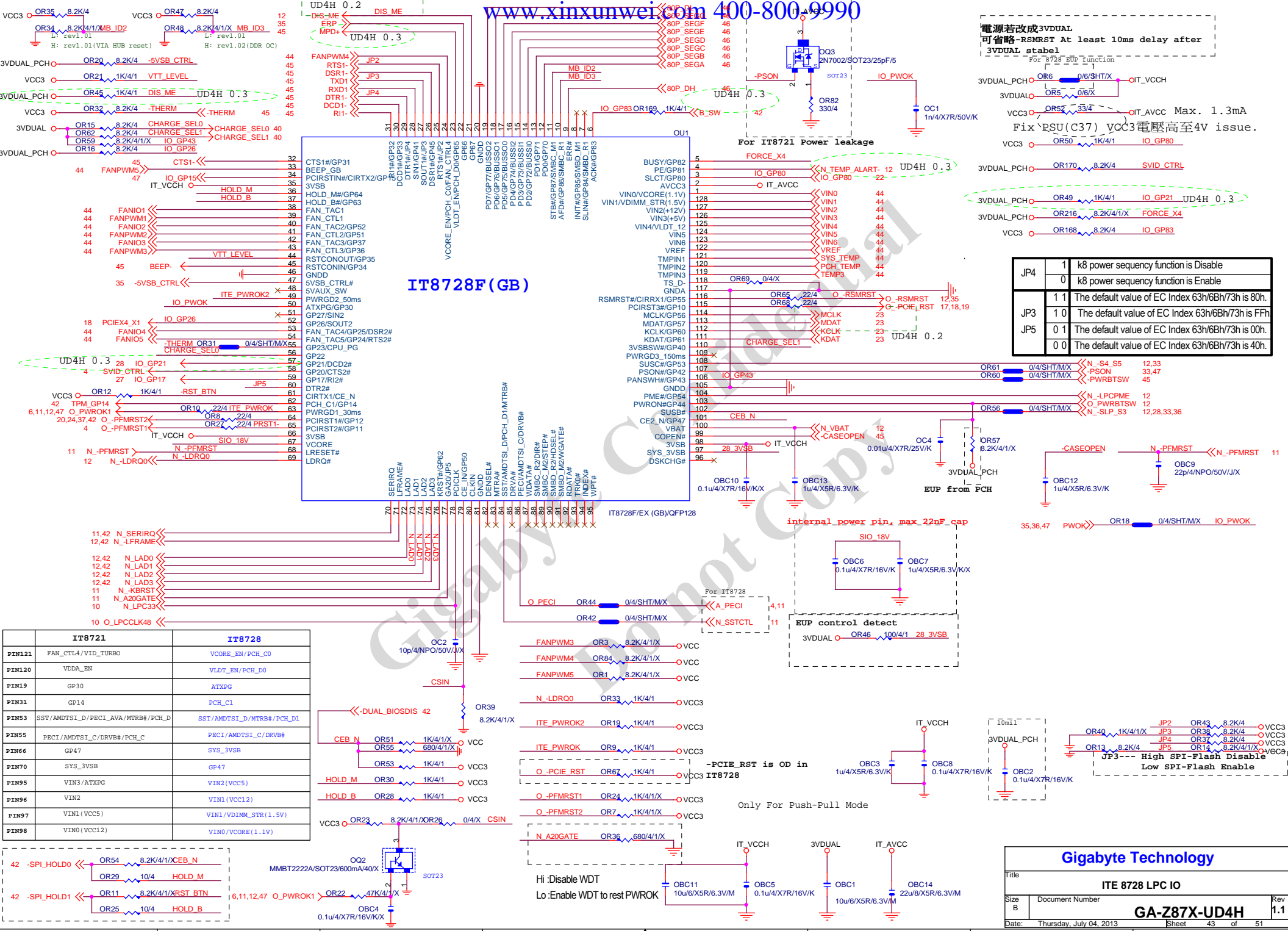


BIOS_SW	
1	Main_BIOS
2	Backup_BIOS

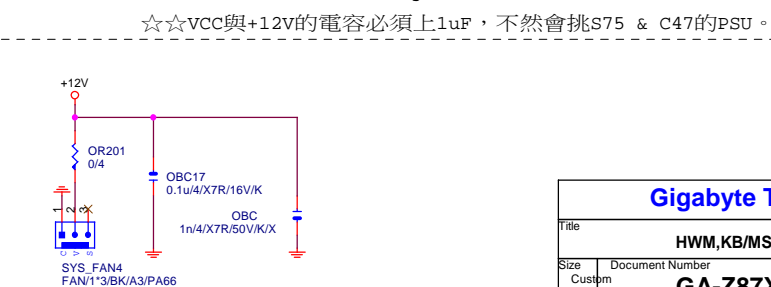
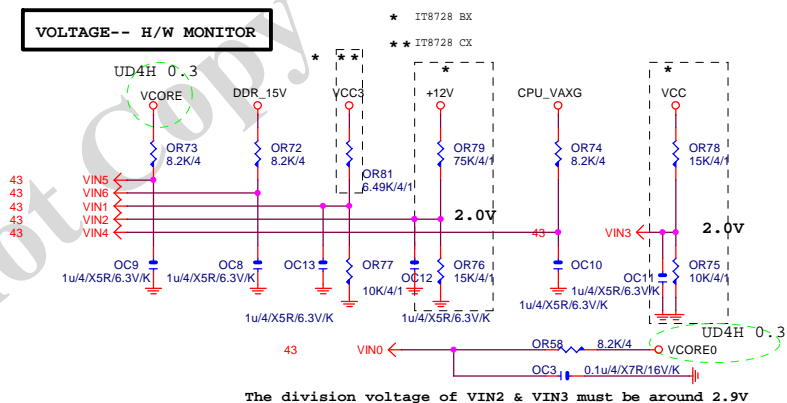
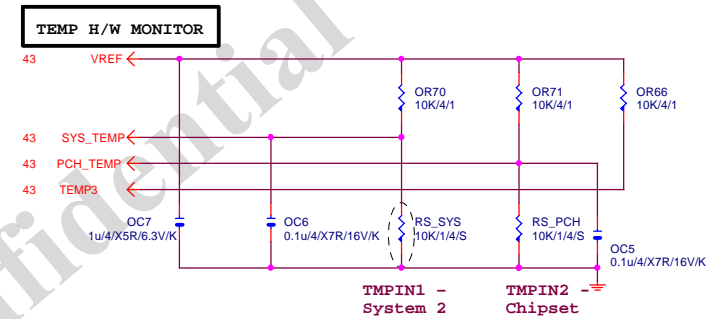
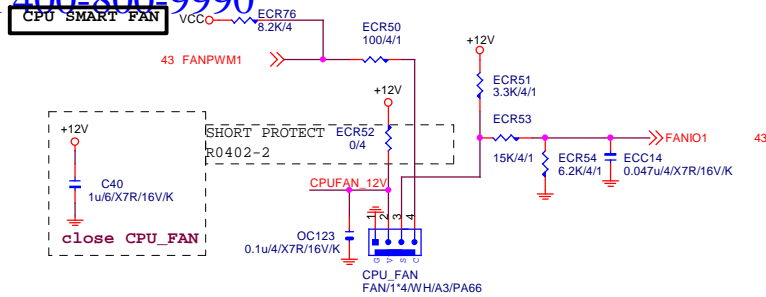


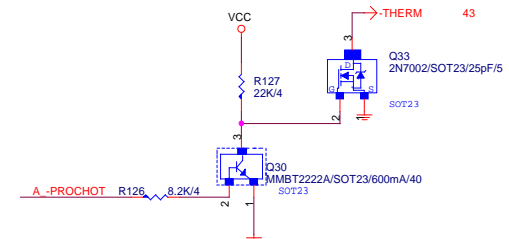
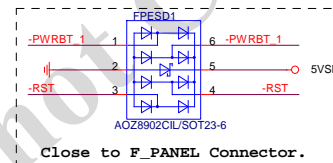
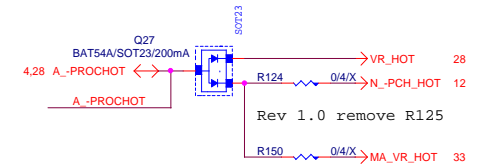
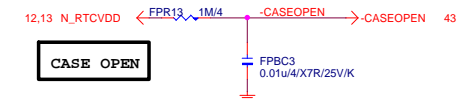
SB	
1	Disable
2	Enable

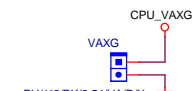
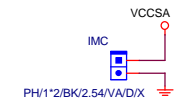
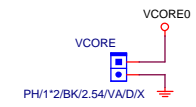
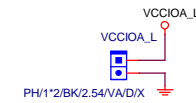
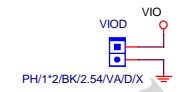
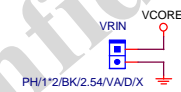
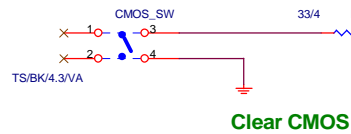
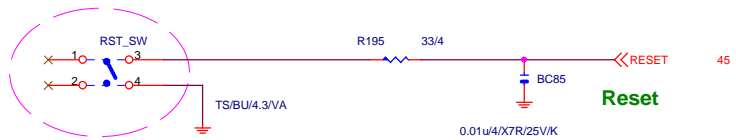
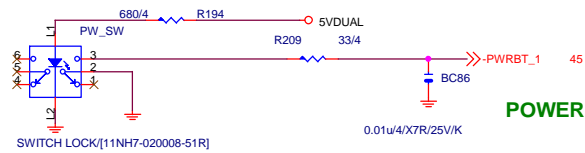
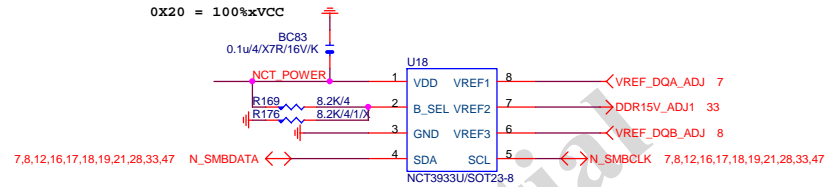
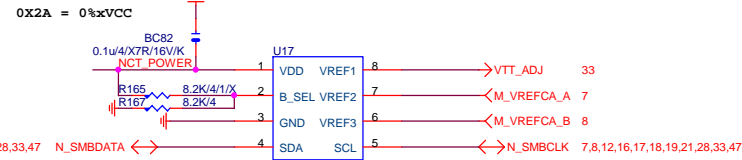
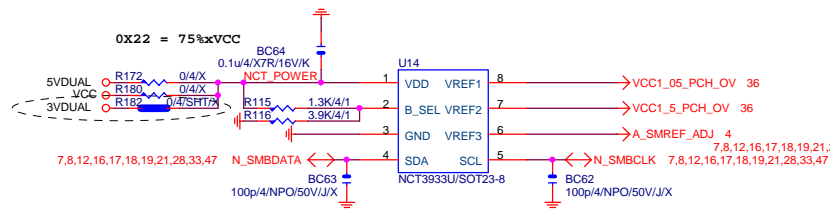
Gigabyte Technology		
Title Dual BIOS		
Size Custom	Document Number	GA-Z87X-UD4H
Date: Thursday, July 04, 2013	Sheet 42 of 51	Rev 1.1



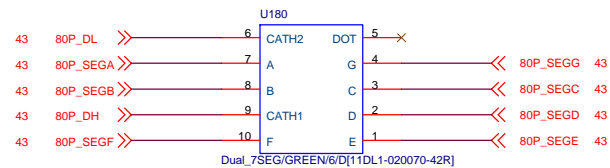
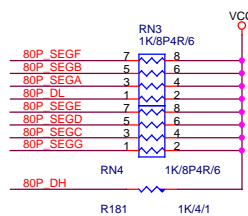
JP4	1	k8 power sequency function is Disable
	0	k8 power sequency function is Enable
JP3	1 1	The default value of EC Index 63h/6Bh/73h is 80h.
	1 0	The default value of EC Index 63h/6Bh/73h is FFh
JP5	0 1	The default value of EC Index 63h/6Bh/73h is 00h.
	0 0	The default value of EC Index 63h/6Bh/73h is 40h.



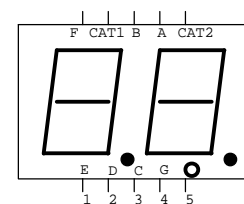




80 PORT



COMMON CATHODE

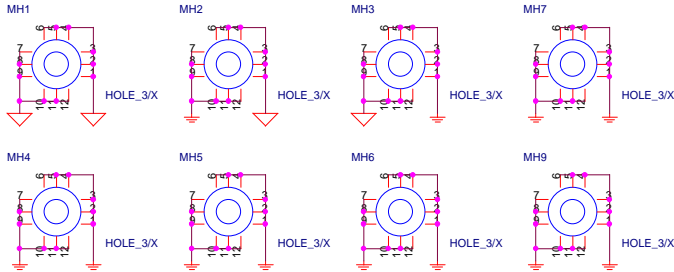
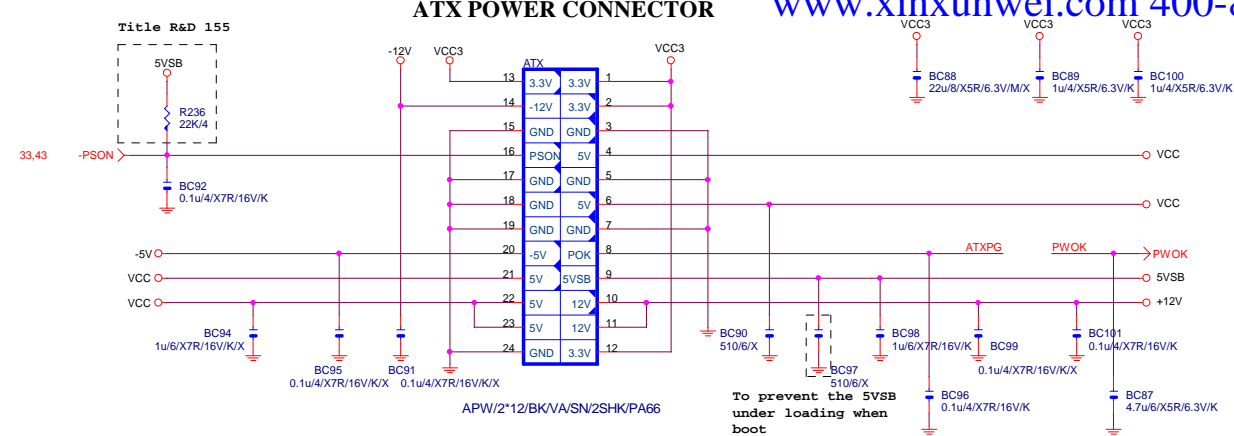
Physical Package  
(TOP VIEW)

GIGABYTE™

Title			RST, PWR, CLR_CMOS	
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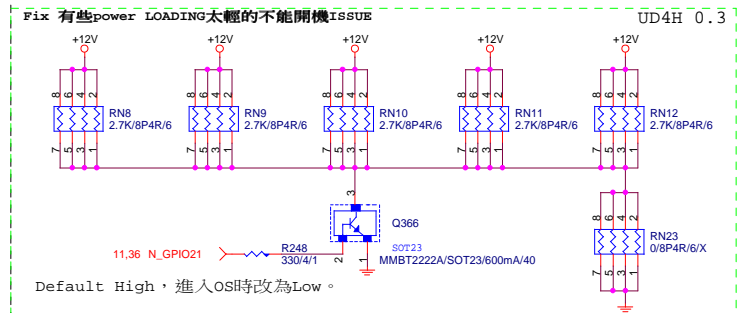
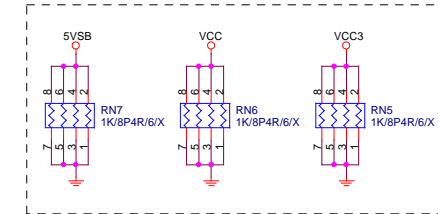
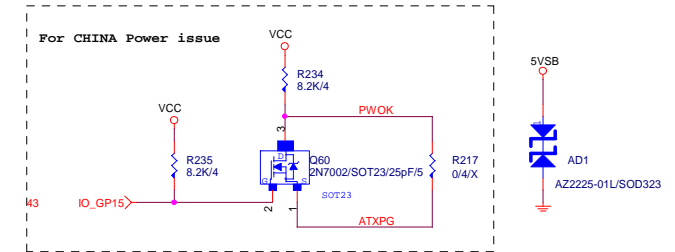
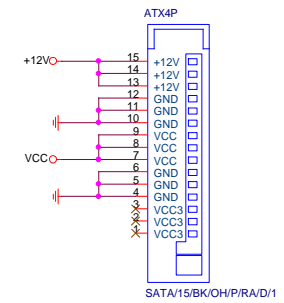
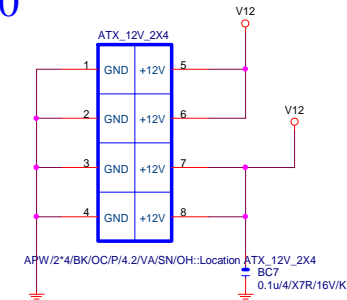
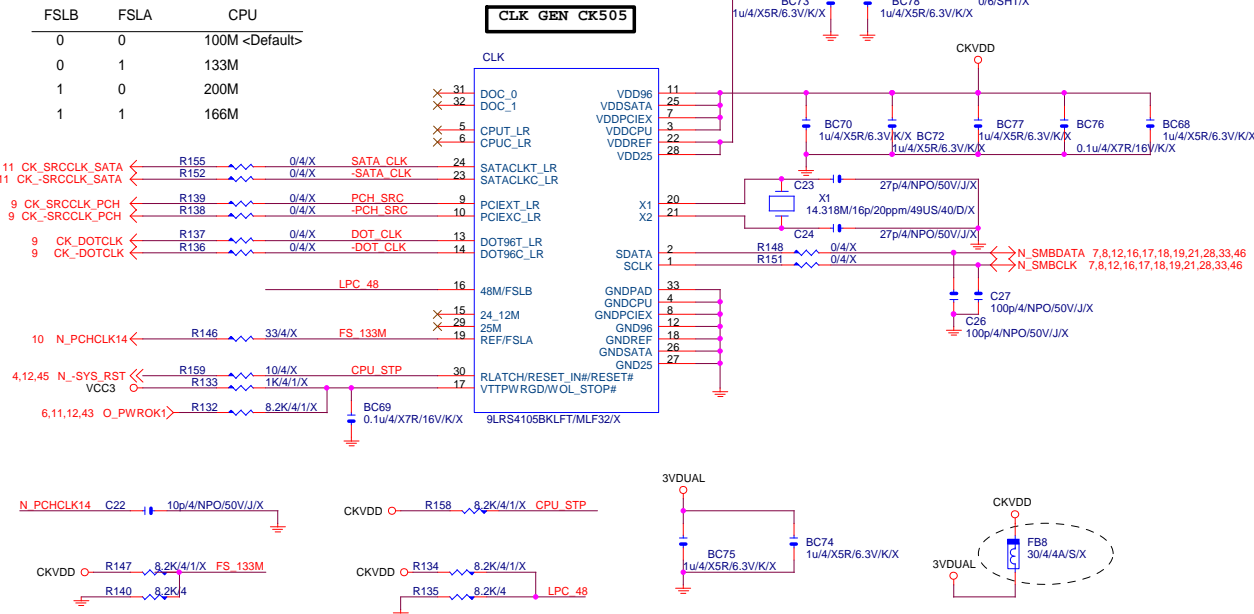
## ATX POWER CONNECTOR

Title R&amp;D 155



## CPU Frequency Selection

FSLB	FSLA	CPU
0	0	100M <Default>
0	1	133M
1	0	200M
1	1	166M



Gigabyte Technology

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Custom		Rev	1.1
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www.xinxunwei.com 400-800-9990

	<b>NOTE</b>
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	<b>NOTE</b>
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[illegible]

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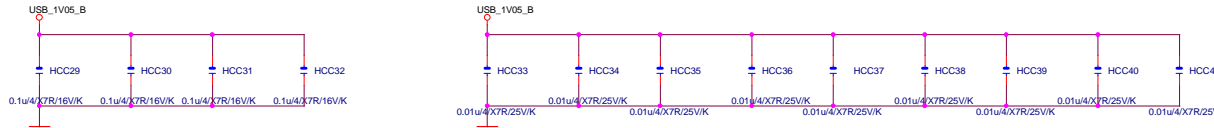
1. 本局在 1997 年 12 月 31 日	2. 本局在 1997 年 12 月 31 日
-------------------------	-------------------------

--	--	--	--	--

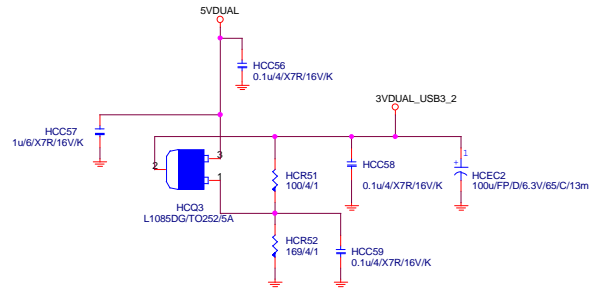
Copyright © 2014

ber

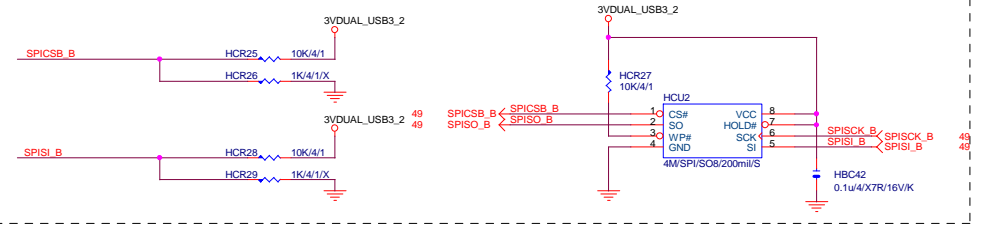
Size	Document Number	Rev
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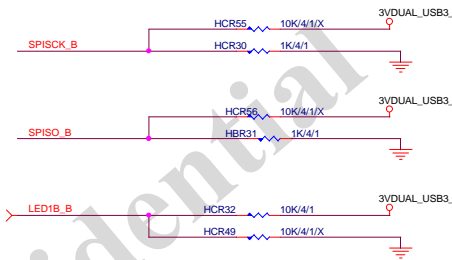
### 3VDUAL\_USB



### # External SPI ROM ; SPI ROM attached mode

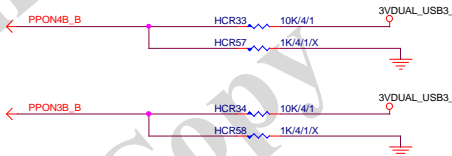


### # Battery Charging

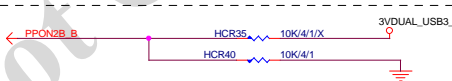


### # Number of Ports ; 4Ports mode

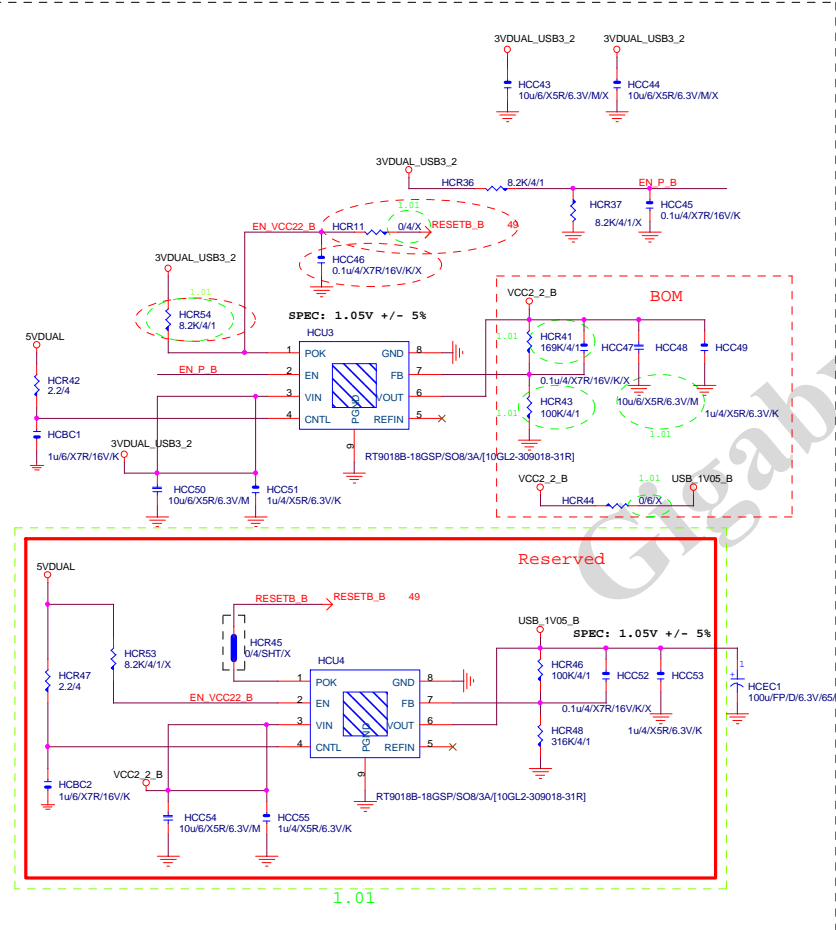
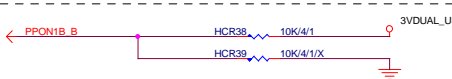
PPON3B / PPON4B : H / H ( 4 port )  
PPON3B / PPON4B : L / L ( 2 port )



### #5 VBUS Power Control ; Individual mode

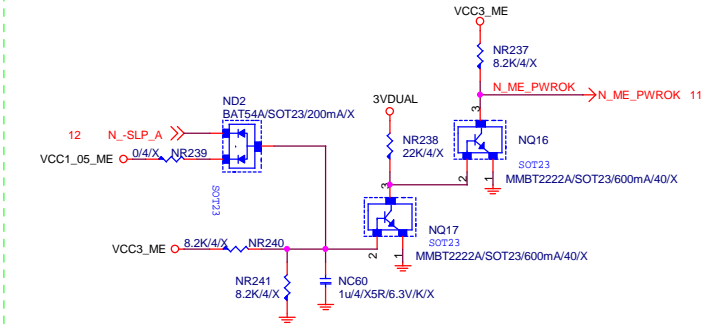
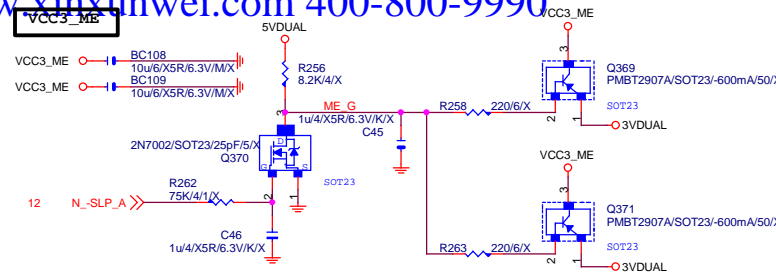
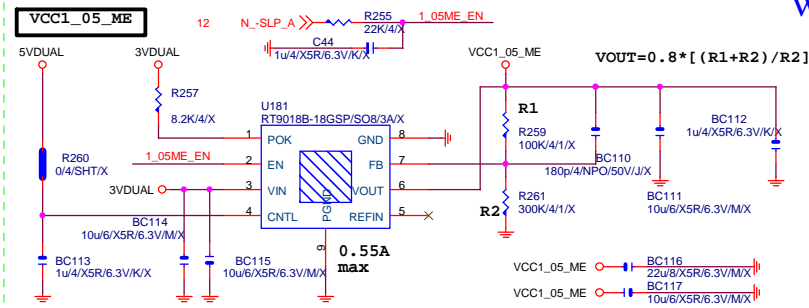


### # PPON1B Pin Function ; Port1 PPONB mode



**GIGABYTE**™

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1.01 remove